



Detectors, DAQ and slow control issues at the SuperFRS

H.Simon

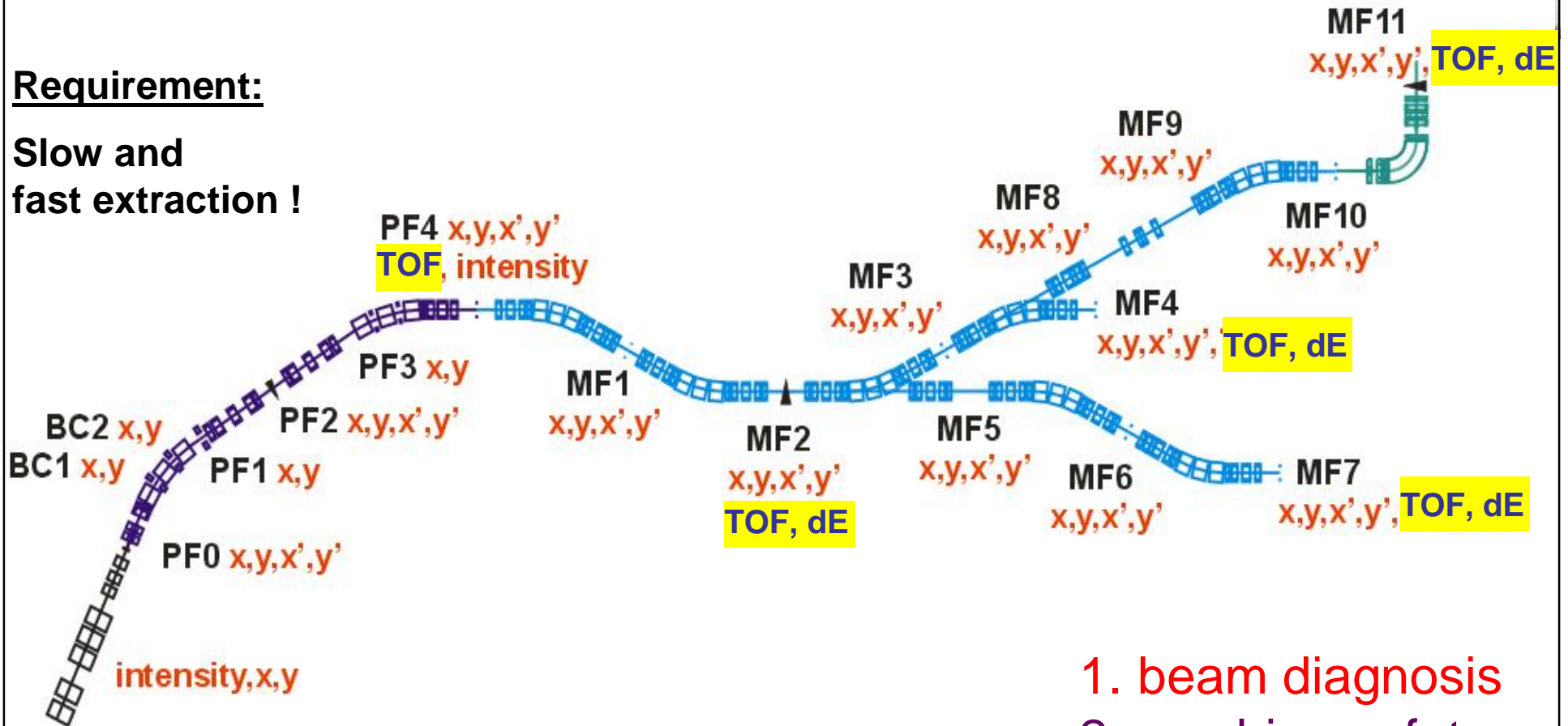
AIMS: detector system used for

1. initial beam steering → ACS/FESA
→ simple, reliable, slow control interface
2. machine safety → ACS/FESA
→ slow control interface, interlocks
3. part of the experiments → MBS/DABC/EPICS
→ interface to DAQ system

Detector Instrumentation of the SuperFRS

Requirement:

Slow and fast extraction !



1. beam diagnosis
2. machine safety
3. experiments

$10^{12}/s$

$<10^{10}/s$

$<10^9/s$

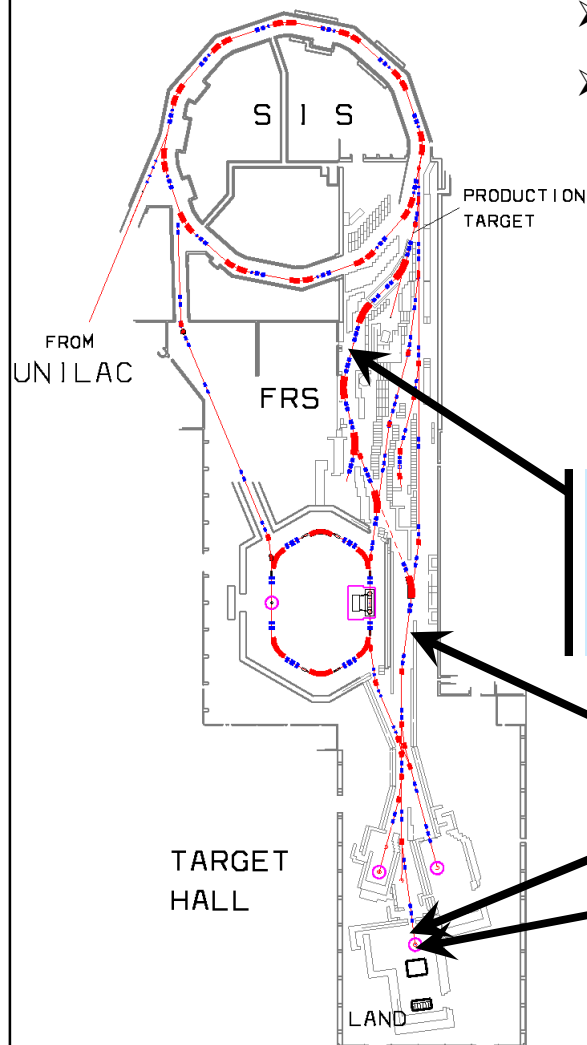
$<10^7/s$

$<10^5/s$

Continuous beam ID is integral part of experiments

Example: ^{132}Sn PDR studies

- Primary: $3 \cdot 10^8$ ^{238}U /spill @550Mev/u
- Secondary (mixed): 50 ions ^{132}Sn /spill

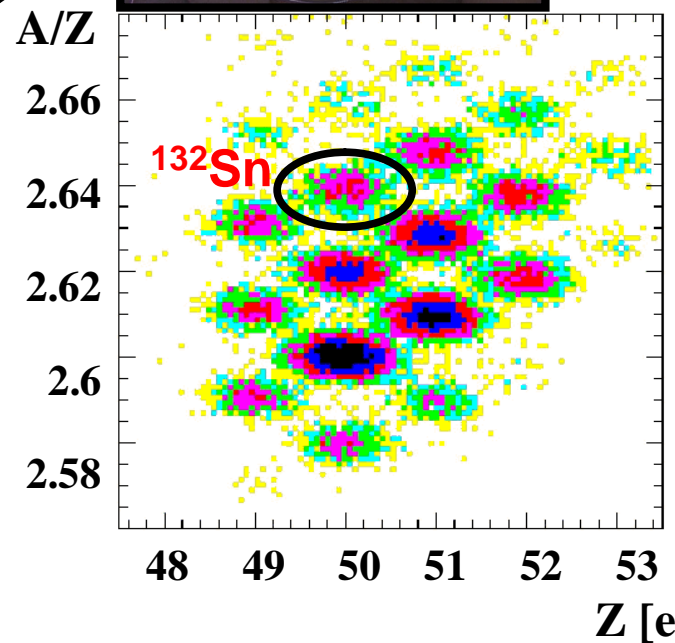
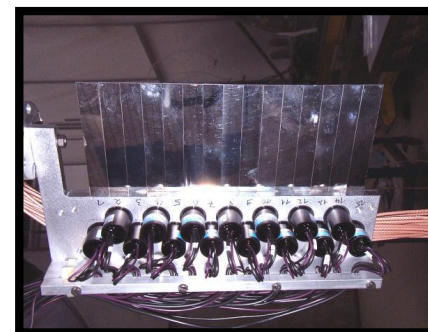


$$\frac{A}{Z} = \frac{m_u c B \rho}{e \beta \gamma}$$

$B\rho$ – from position at middle focal plane of the FRS

β – from TOF

Z – from ΔE



Helsinki - Preparation EoI - Oct 6th

NO CHARGE STATES !

B ρ - ΔE -TOF method: Requirements

$$\begin{array}{l} B\rho = A/Z \cdot \beta \cdot \gamma \quad \rightarrow \quad A/Z, P \\ \text{TOF} = L/\beta \quad \rightarrow \\ \Delta E \sim Z^2/\beta^2 \quad \rightarrow \quad Z \end{array}$$

Pos res. $\sigma \leq 1 \text{ mm}$
Timing res. $\sigma: 50 \text{ ps}$
 ΔE resolution $\sigma: 1\text{-}2 \%$

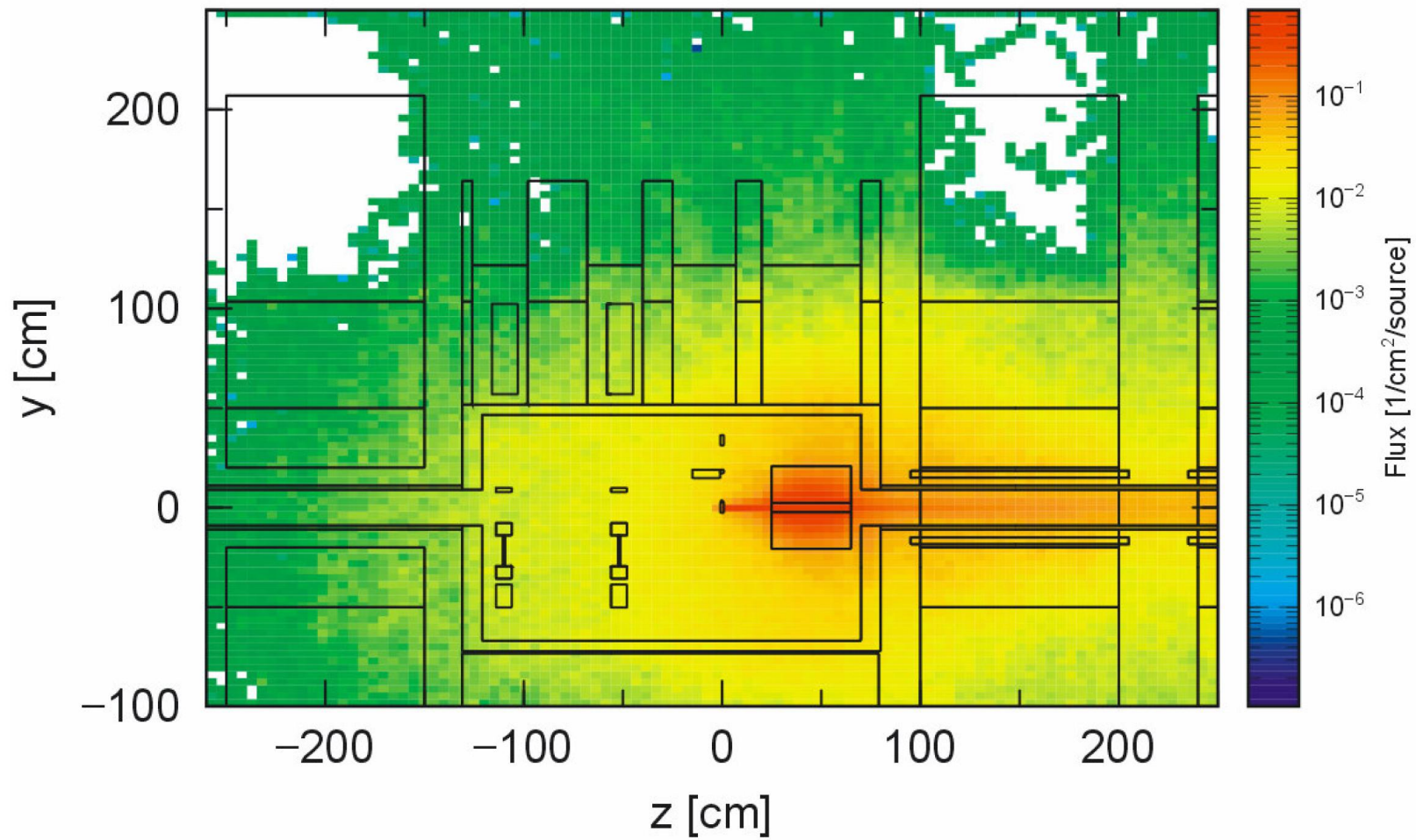
- Position: Wirechambers (single event readout)/Diamond
- ΔE : MUSIC/TEGIC
- TOF: Plastic/Diamond



Standard detectors at the **FRS**

- **Beam diagnostics** : Current Grid (CG)
- **Intensity** : Secondary Electron Emission Transmission Monitor (SEETRAM)
- **x,y** : Multi Wire Proportional Chamber (MWPC)
 ΔE : Multi Sampling Ionization Chamber (MUSIC)
Tof : Scintillators

Radiation environment target area



Detector Scheme for Super-FRS target area

available/possible systems

Fast extraction

Resonance Transformer

Diamond

(single crystal, current readout)

Pickups

Beam induced fluorescence(BIF)

Rest Gas Monitor (RGM)

Current Grids

Camera on target (IR)

Intensity

Position

Profile

Monitoring

Slow extraction

Cryogenic Current Comparator
(SQUID)

SEETRAM

Diamond (poly crystal & particle)

BIF

RGM

Current Grids/Wire chambers

Camera on target (IR)

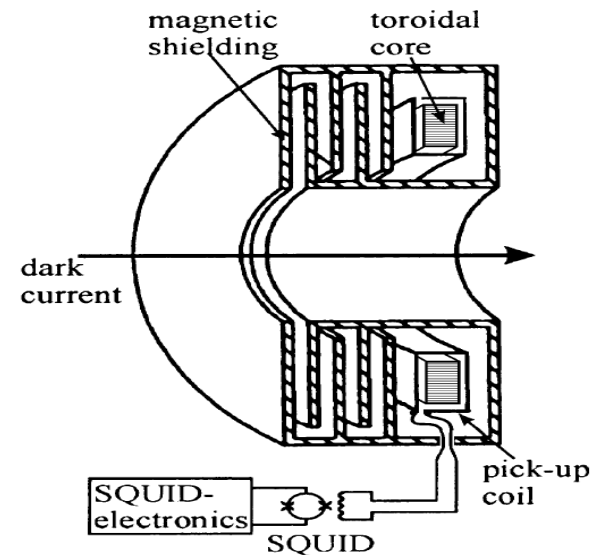
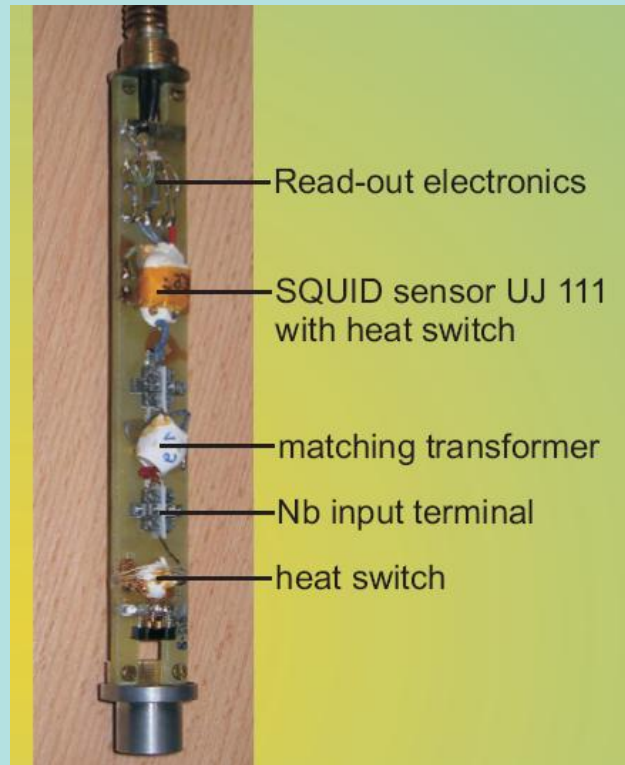
full intensity | *reduced intensity (< about 1 nA)*

Cryogenic Current Comparator

W. Vodel , R. Neubert , S. Nietzsche , R. Nawrodt , K. Knaack , K. Wittenburg
A. Peters

- About 100 k€ / system
- > 1nA, pulse to DC

The design of the CCC is realized as co-operation of DESY Hamburg, Jena University and GSI Darmstadt.

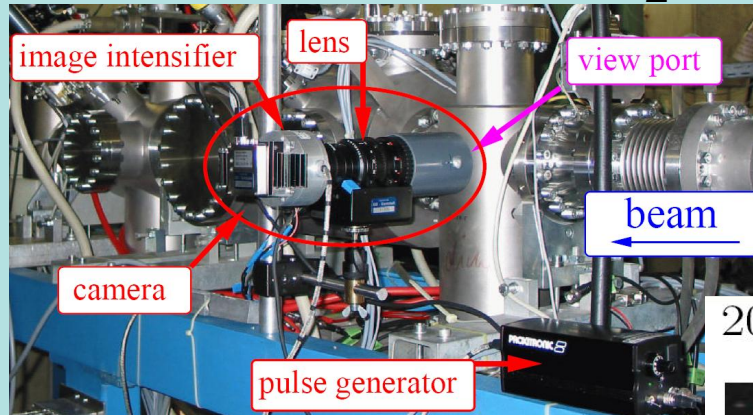


- R&D: Radiation Hardness/Shielding of SQUID

Beam Induced Fluorescence

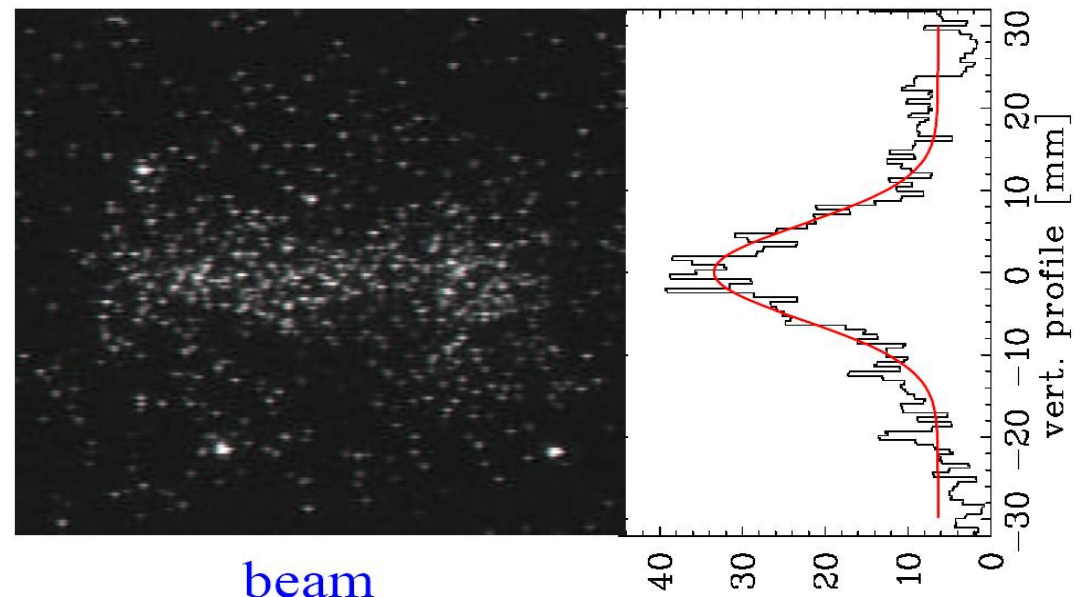
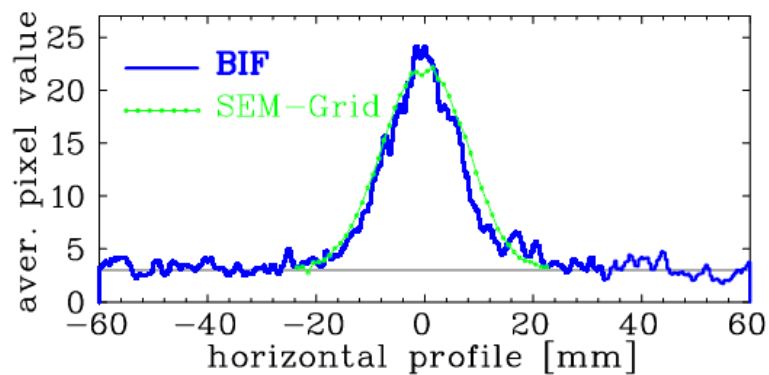
P. Forck / GSI

- about 100 k€ / system
- Optical imaging of a N₂-filled test volume (MCP + camera)



@UNILAC

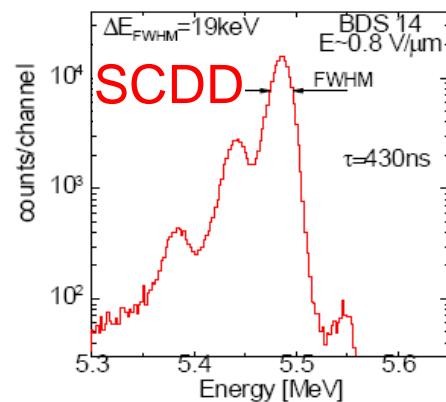
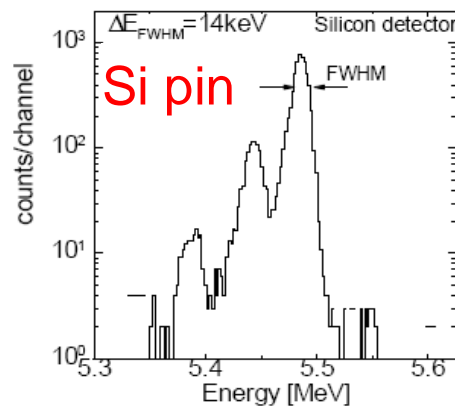
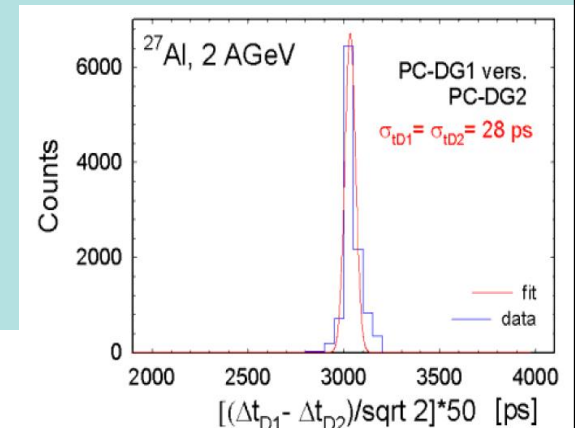
200 μ s Ar¹¹⁺ beam of $I = 700 \mu$ A with 6 MeV/u



beam

Throughout the separator: Diamond Detectors

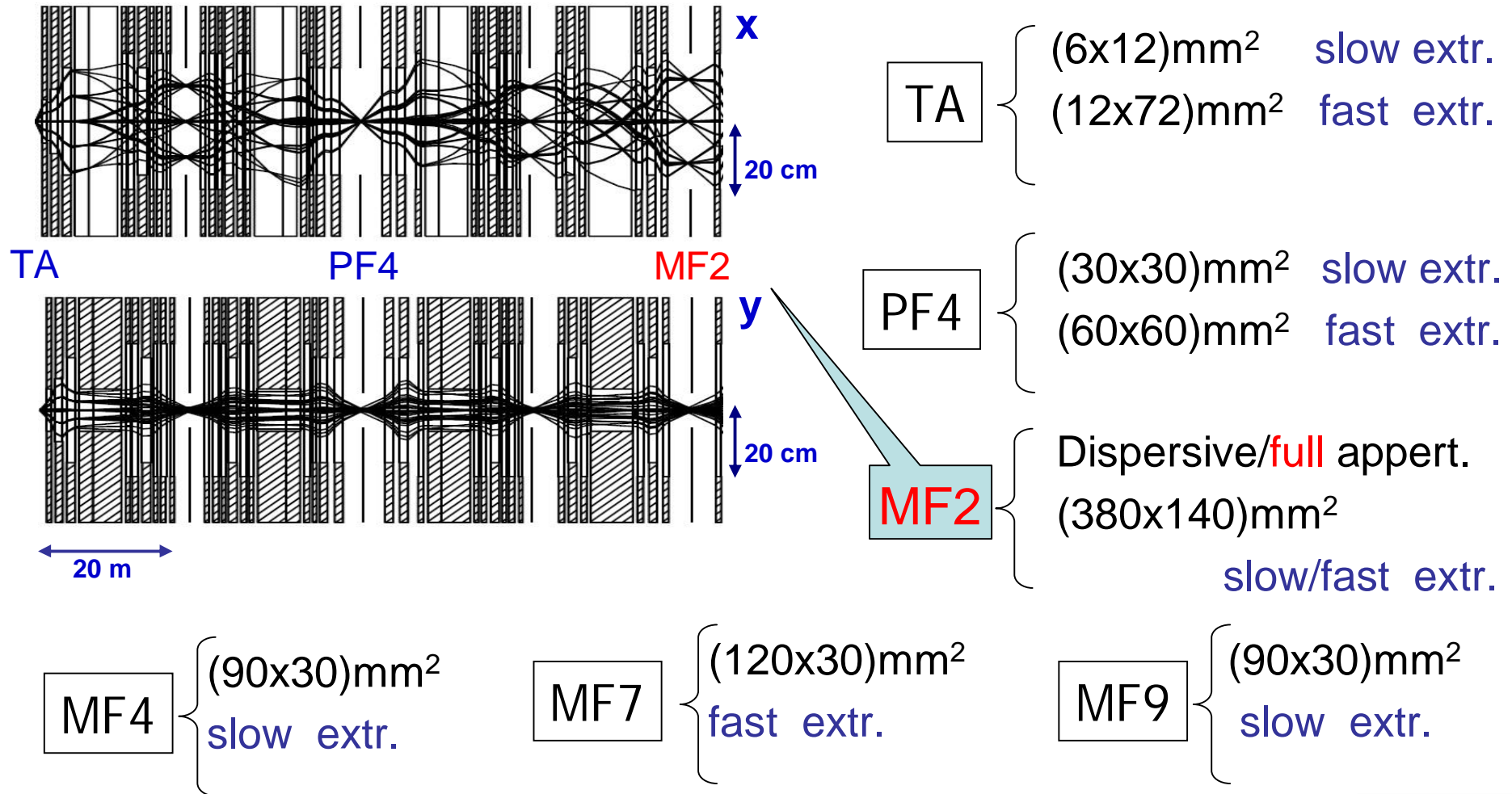
- current readout for single crystal (a few mm²)
- cheaper polycrystalline diamonds (a few cm²)
- very good homogeneity and radiation hardness
- price from a few 100 €/cm² to 1000 €/cm²
- expertise inhouse



M. Pomorski,
E. Berdermann
et al. Nordhia, RD42

Detector sizes: **Super-FRS**

→ dipole gaps 140mm



Bookkeeping (i)

2.4.6 Diagnostics

Fluorescent Screen		
Number of elements		1
Overall length	mm	50
Horizontal aperture	mm	100
Vertical aperture	mm	100

CVD-DD (diamond detectors, calibration)		
Number of elements		1
Overall length	mm	100
Horizontal aperture	mm	100
Vertical aperture	mm	100
Rate	Hz	$1 - 500 \cdot 10^6$

Luminosity Monitor (SEETRAM)		
Number of elements		2
Overall length	mm	200
Horizontal aperture	mm	100
Vertical aperture	mm	100
Intensity range	particles/spill	$< 10^{11}$

Bookkeeping (ii)

Position Monitor (CG)		
Number of elements		32
Overall length	mm	300
Horizontal aperture	mm	400
Vertical aperture	mm	250
Intensity range (energy deposition)	mW/mm	<100

Tracking Detector (MW)		
Number of elements		32
Overall length	mm	300
Horizontal aperture	mm	400
Vertical aperture	mm	250
Rate	kHz	<100

Capacitive Pick-up		
Number of elements		2
Overall length	mm	300
Horizontal aperture	mm	150
Vertical aperture	mm	150
Intensity range	particles/spill	<10 ⁹

MUSIC Detectors		
Number of elements		4
Overall length	mm	500
Horizontal aperture	mm	400
Vertical aperture	mm	80
Rate	kHz	200 ... 1000

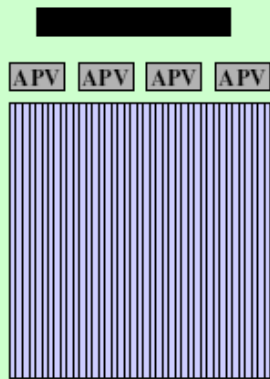
ToF (diamond detectors, PC-CVD-DD)		
Number of elements		4
Overall length	mm	200
Horizontal aperture	mm	400
Vertical aperture	mm	50
Pitch	mm	1
Time resolution	ps	<50

R³B diamond detector layout:

→ MF2 SuperFRS: × 8(h)

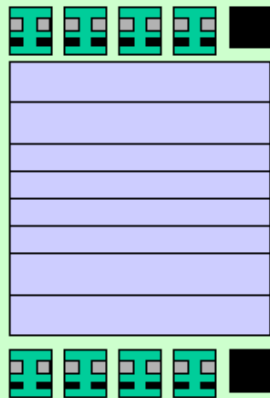
Test exp. 04/08

R. Gernhäuser (TU-München)



tracking layer:

- 50 x 50 mm, d = 100 μm, PC-CVDD
- 140 μm pitch (125 μm strips, 15 μm gap)
- only digital position information
- multiplexed readout in vacuum



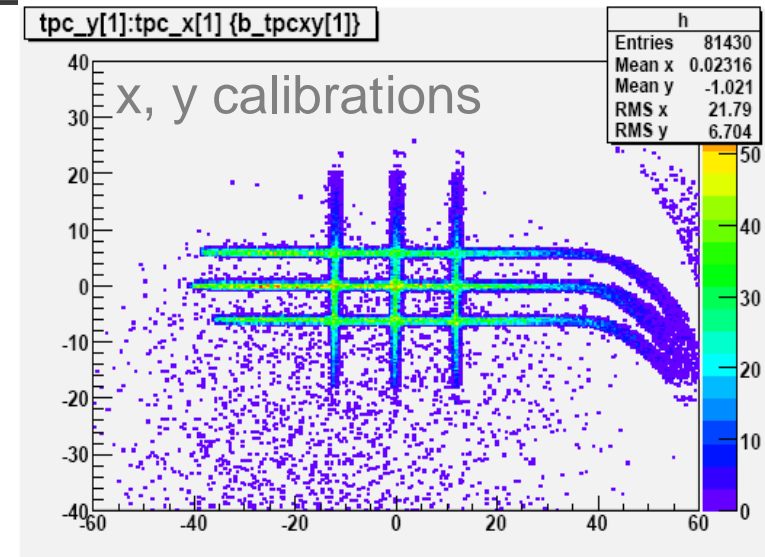
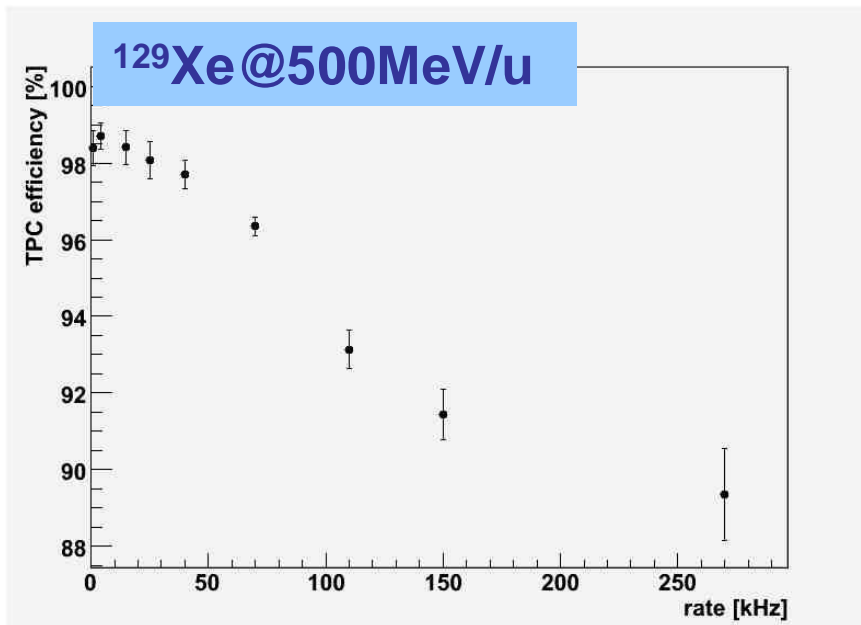
timing layer:

- 50 x 50 mm, d = 100 μm, PC-CVDD
- 8 rate matched strips, y information, trigger
- analog preamplification in vacuum
- discriminator @ 5 m distance

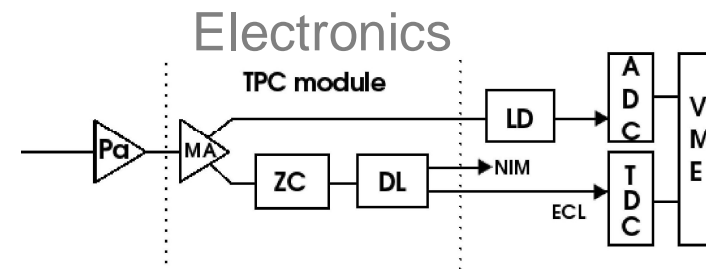
Time Projection Chamber

- CUB Bratislava

- (240x100) mm² active area
- Gas P10 at 1 atm
- Integrated delay lines (2x-pos, 4y-pos)
- $\sigma_x \sim 0.1$ mm, $\sigma_y \sim 0.05$ mm



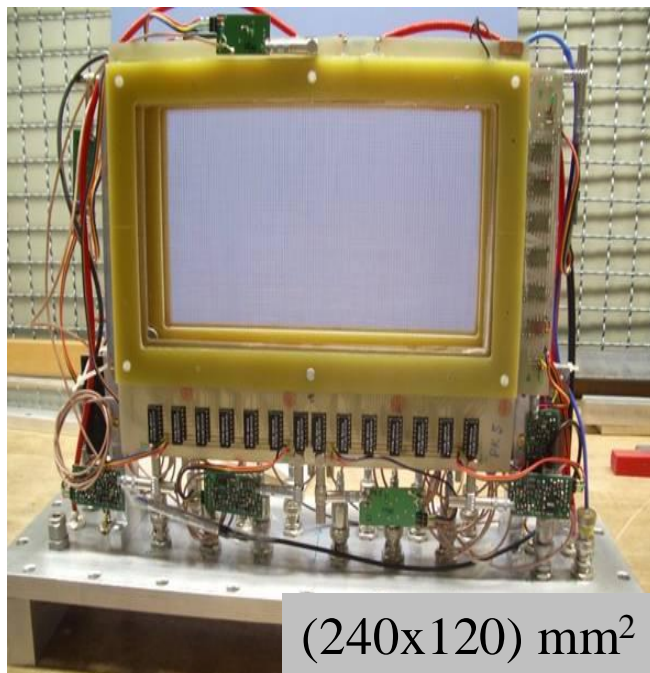
- VME standard electronics
- 90% efficiency at ~100kHz



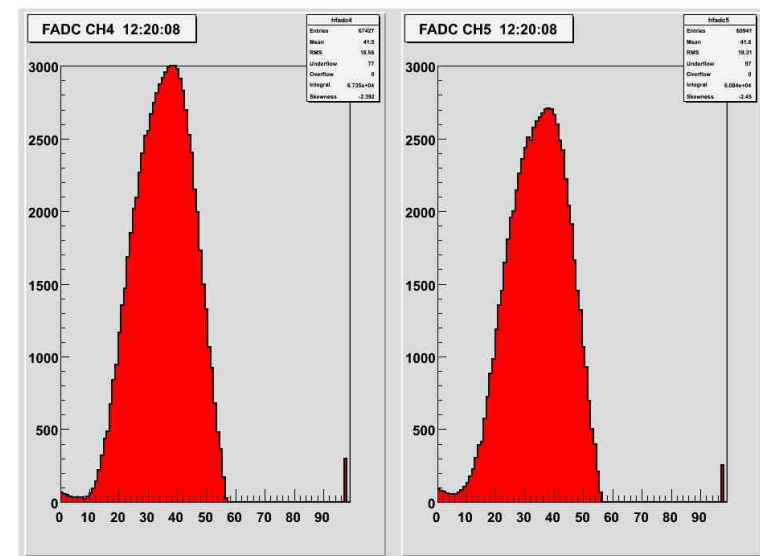
Beam Profile Detector - CUB Bratislava

for intense fast extracted and slow extracted beams

- Basic module (120x120) mm²
- 5mbar < gas Ar+(10%)CO₂ < 1bar
- Wires 2mm pitch directly connected to delay lines



Beam profile

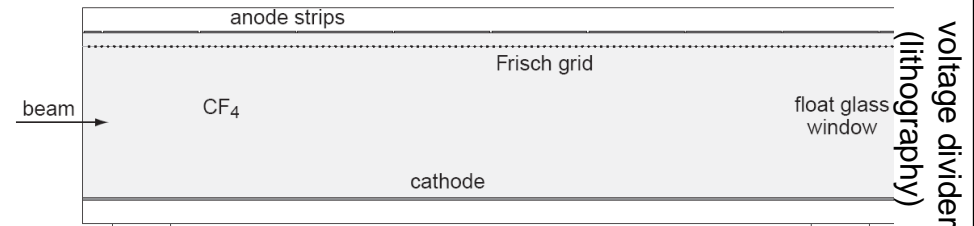


¹²C@200-400 MeV/u
 10⁴- 1.6 · 10⁹ ions/spill
 Spill length: 300 ns
 FADC SIS3301(100MHz)

Missing items:

FRS MUSIC

- Fast ΔE counter
100 kHz – 1MHz, res. 1-2%,
large dynamic range
(no MIPS Z \rightarrow \sim 100)
 - TEGIC (RIKEN, ca 1MHz)
 - Silicon stacks ?
 - sc-CVDD ?
- Fast Tracking detectors
large dynamic range
 - PC-CVDD with
continous readout
 - ?
- Fast TOF (currently SCI, ca. 10MHz)
large dynamic range
 - PC CVDD
 - ?



Geometry

active area:	200 mm \times 80 mm
active length:	400 mm
anode:	8 anode strips with 50 mm active length each
total gas length:	420 mm
entrance windows:	float glass D263 (DESAG), thickness 210 μ m
distance anode – grid:	7 mm
grid wire diameter:	100 μ m
grid wire spacing:	1 mm

Gas supply

counting gas:	CF ₄ (tetrafluoromethane)
counting gas pressure:	atmospheric pressure (\approx 1 bar)
maximum differential pressure:	\pm 5 mbar to atmospheric pressure
total gas volume:	8.6 l
recommended gas flow:	4 – 6 l/h
typical drift velocity:	10–12 cm/ μ s @ 1kV/cm for electrons
gas supply connectors:	Swagelok tube fittings, 8 mm

High voltage supply

maximum input voltage:	-10 kV
operating input voltage:	-8 kV – -10 kV
field homogenization current:	32 μ A @ 10 kV
high voltage connector:	SHV connector

\sim 200 kHz

Missing items:

TEGIC

- Fast ΔE counter
100 kHz – 1MHz, res. 1-2%,
large dynamic range
(no MIPS Z \rightarrow \sim 100)
 - TEGIC (RIKEN, ca 1MHz)
 - Silicon stacks ?
 - sc-CVDD ?
- Fast Tracking detectors
large dynamic range
 - PC-CVDD with
continous readout
 - ?
- Fast TOF (currently SCI, ca. 10MHz)
large dynamic range
 - PC CVDD
 - ?

Beam 



K. Kimura et al., Nucl. Instr. and Meth. A538(2005)608

P10 425mm normal pressure
Electrodes(anode/cathode) $4\mu\text{m} \times 25$ Mylar
 14 mg/cm^2
Distance(anode-cathode) 2cm
Detector Window $150\mu\text{m}$ Kapton

\sim 1 MHZ



Readout using MBS

- Basic system (<http://daq.gsi.de>)
 - Trigger module + VME processor Modules (CAMAC, VME)
- Integrates foreign DAQ Systems (via Time Stamps)
- various FEE integrated
via GTB/SAM (Bus / DSP&FPGA VME board)

lightweight, scalable, N x M, full VME speed
allows for staged transition !!!

SuperFRS, R³B, PRESPEC(High/Despec)

APV Frontend (triggered) - M. Böhmer, TUM

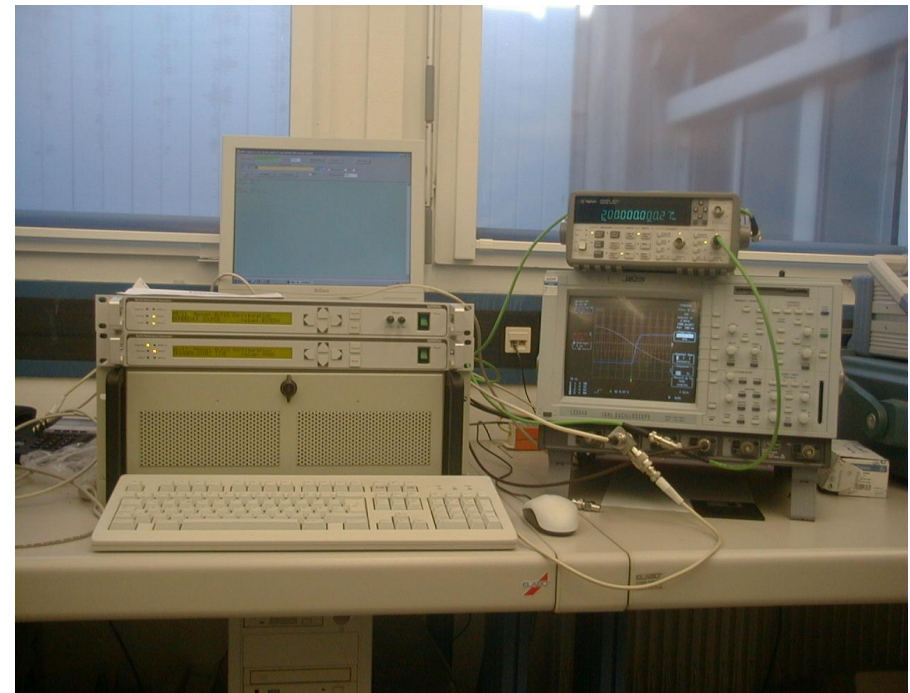
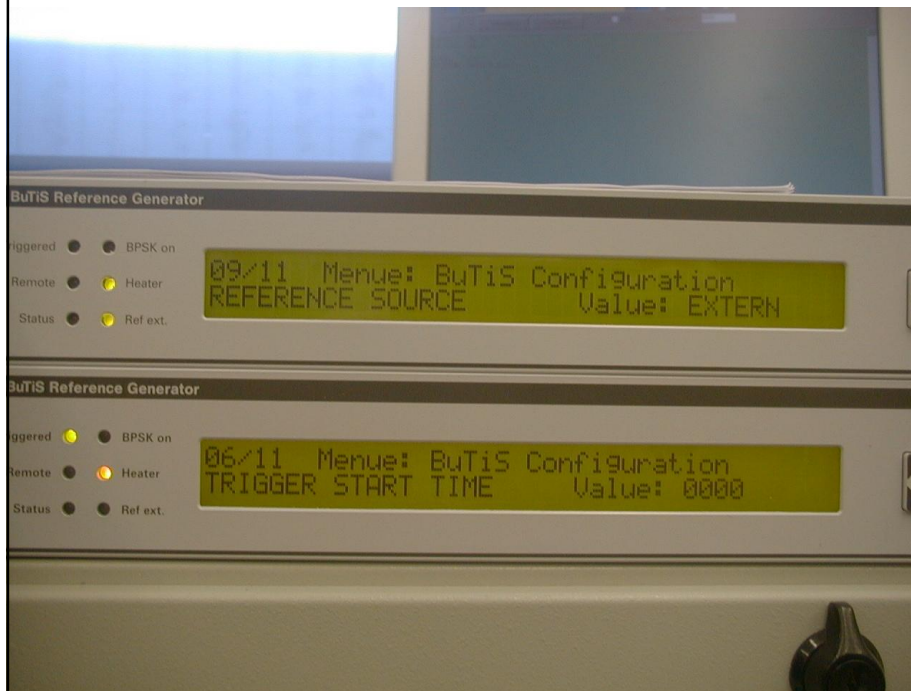


- APV25-S1 RAL
→ CMS (Si, ...)
(128 channel analogue pipeline
192 columns analogue storage.
50 ns shaped pulses
100mV / 25,000 electrons
40MHz sample
Useful data marked
test pulser, pos/neg, ...)
- I²C control
- Clck, Trg
- Low power
consumption
- Readout to MBS
(ADC/FPGA/DSP)

Extended Time distribution (BuTiS)

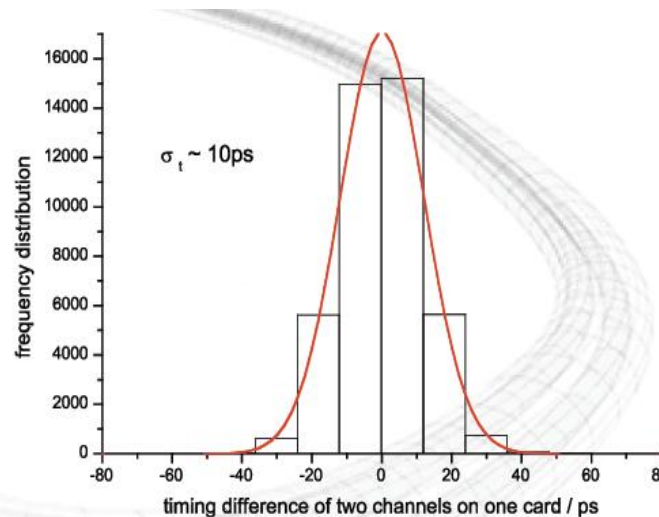
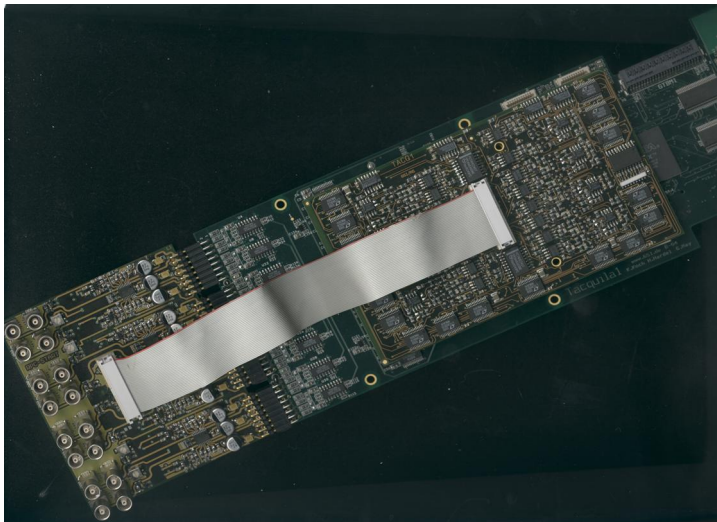
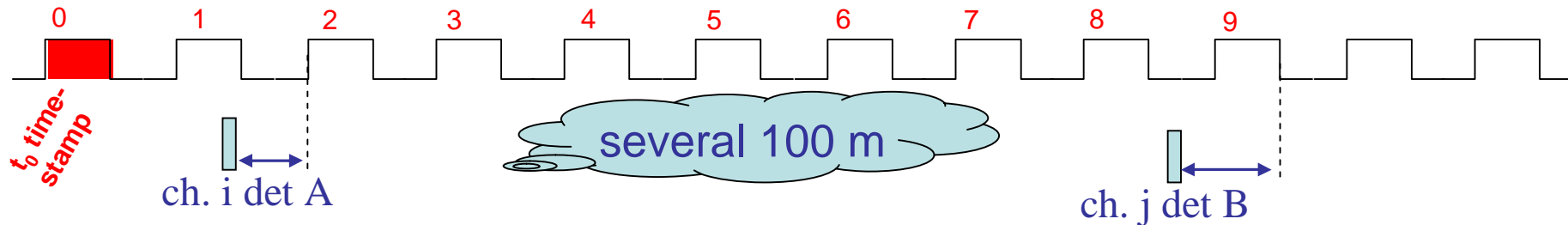
P.Moritz/GSI

- Campus wide time distribution via fibre optics
- Synchronized local oscillators (100kHz, 10Mhz, and e.g. 200, 155 or 76 Mhz) with ± 100 ps/km absolute uncertainty and $\ll 10$ ps oscillator jitter



Precision timing (<50ps) vs. **Campus Clock**

- avoid extended cabling and dead time domains
→ free running time stamped systems **SuperFRS -- Caves**



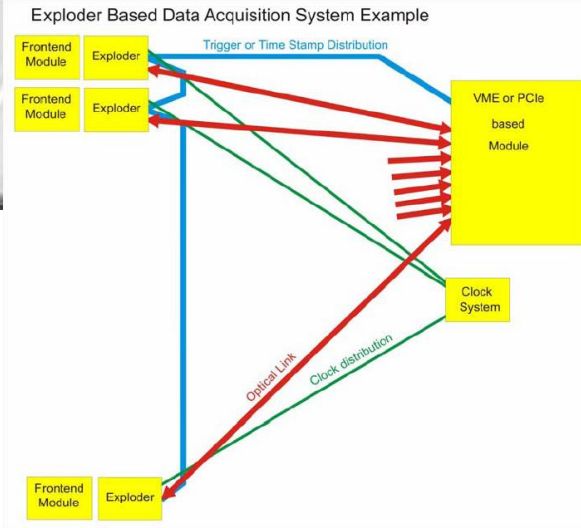
Timing FEEs:

Tacquila system
(ASIC FhG/GSI)

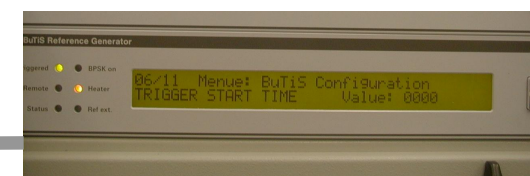
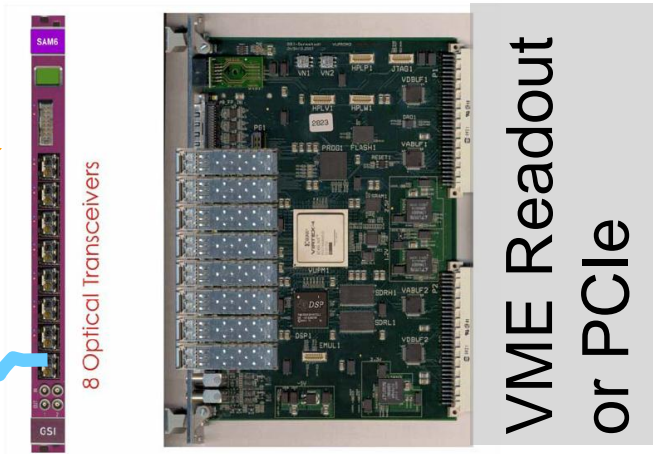
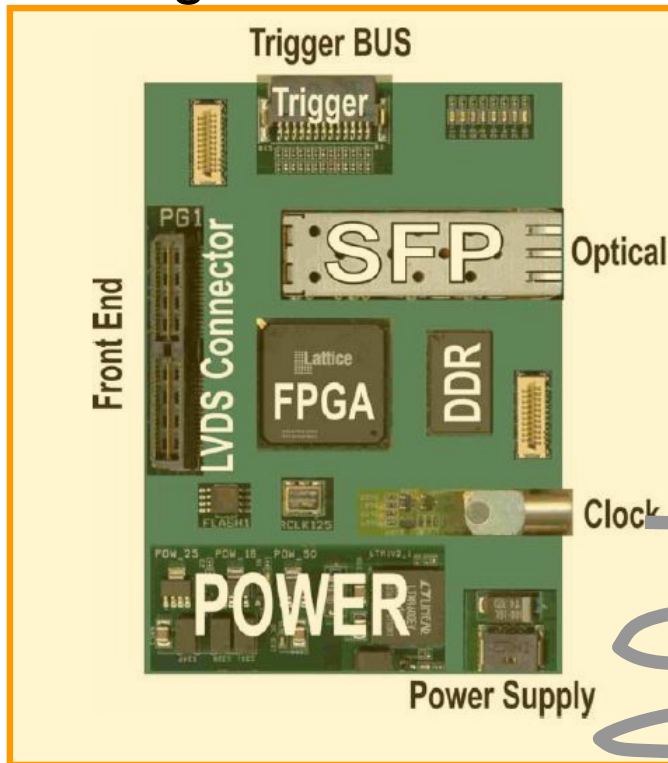
New systems
(ASIC dev. GSI)

Readout Chain revisited ...

- NUSTAR uses MBS, a (in principle) triggered DAQ system ...
- ➔ preprocessing



any LVDS input



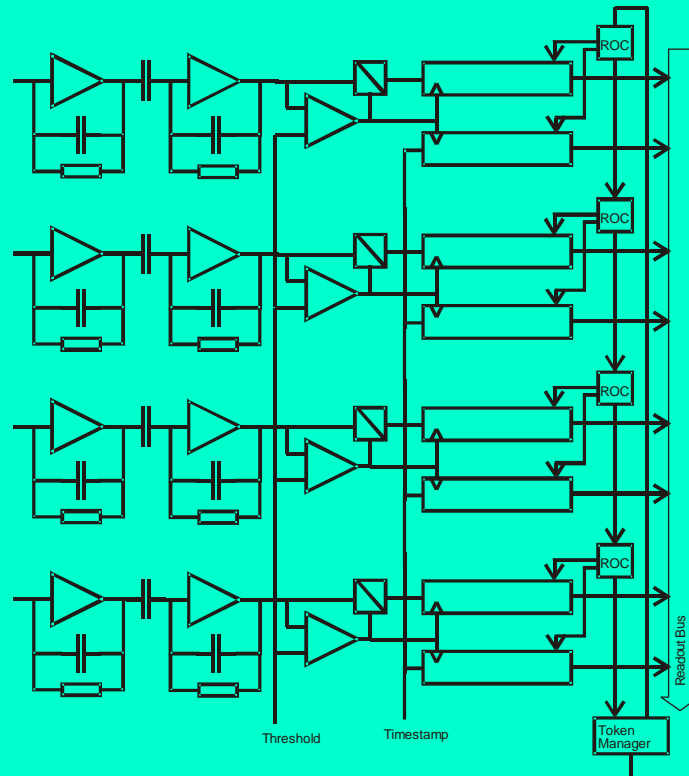
Token Ring Scheme (NXYTER)

→ “deadtime free”

Ch. Schmidt (GSI)



Sparse & derandomized readout



- Periodic readout at 20MHz
- Token asynchronously passes from channel to channel in search of data
- Within one readout cycle token could pass through all channels
- If token encounters occupied channels, data readout is initiated.
- After readout the token passes to the next channel.

→ 20 MHz/128 Ch \approx 160 kHz

ENOB 10.4

Ulrich Trunk
Physikalisches Institut der Universität Heidelberg



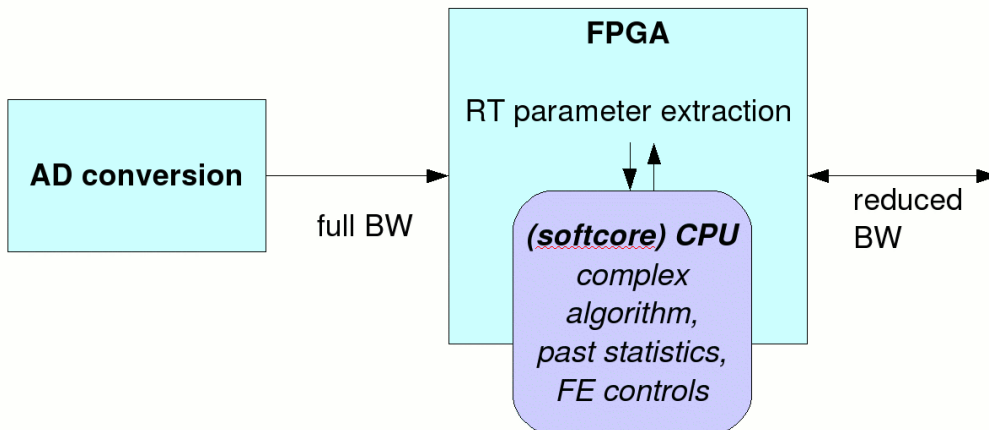
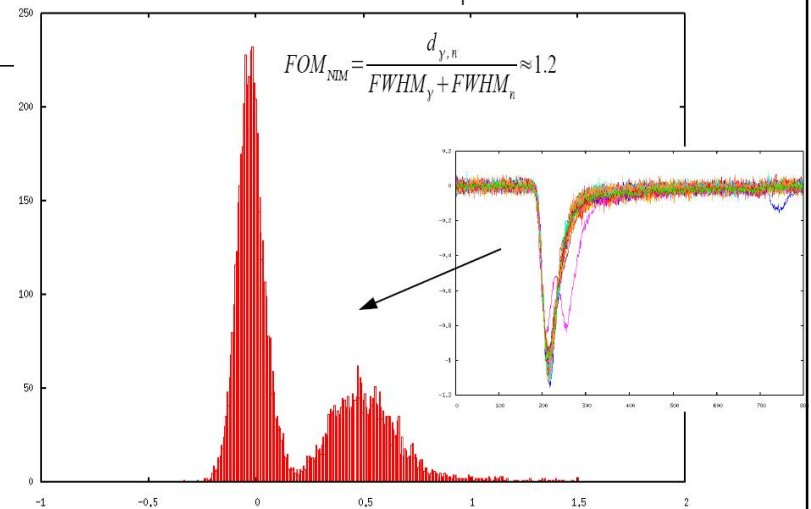
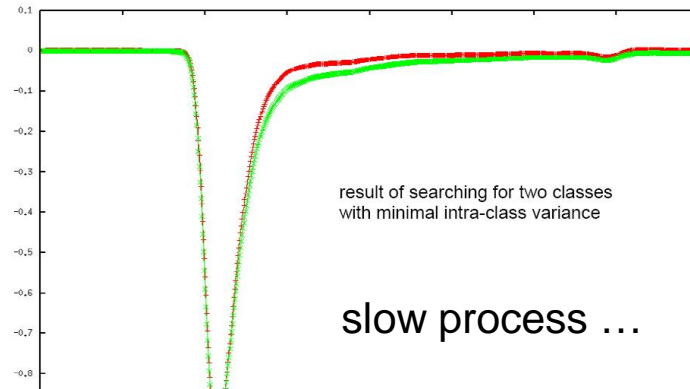
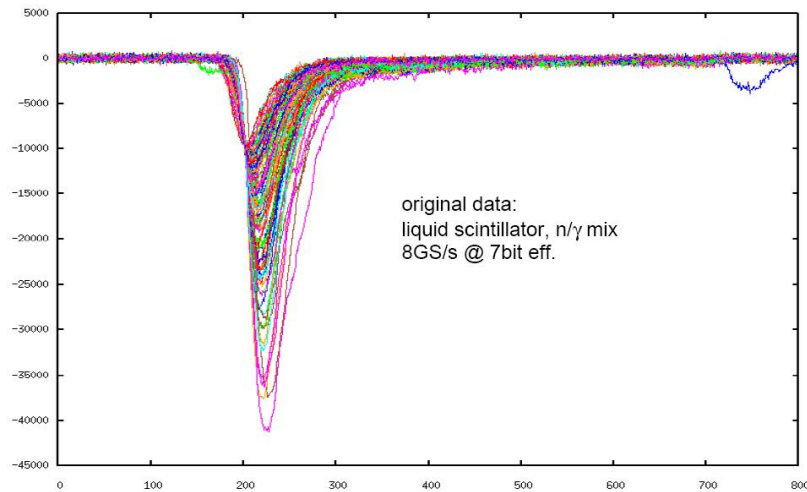
Variety of applications: Test with single wire readout foreseen !



Digital Signal Processing

(PULSE SHAPE)

M. Vencelj et al. (JSI)

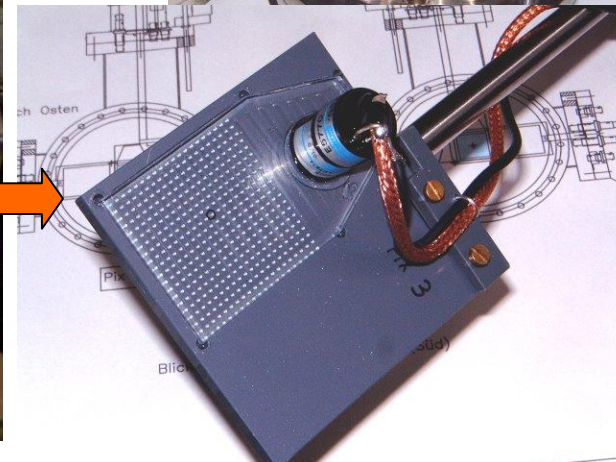
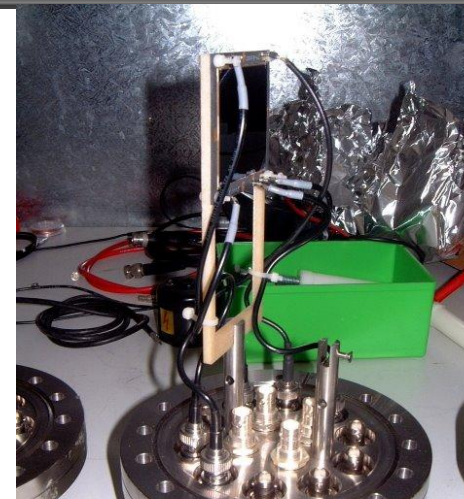
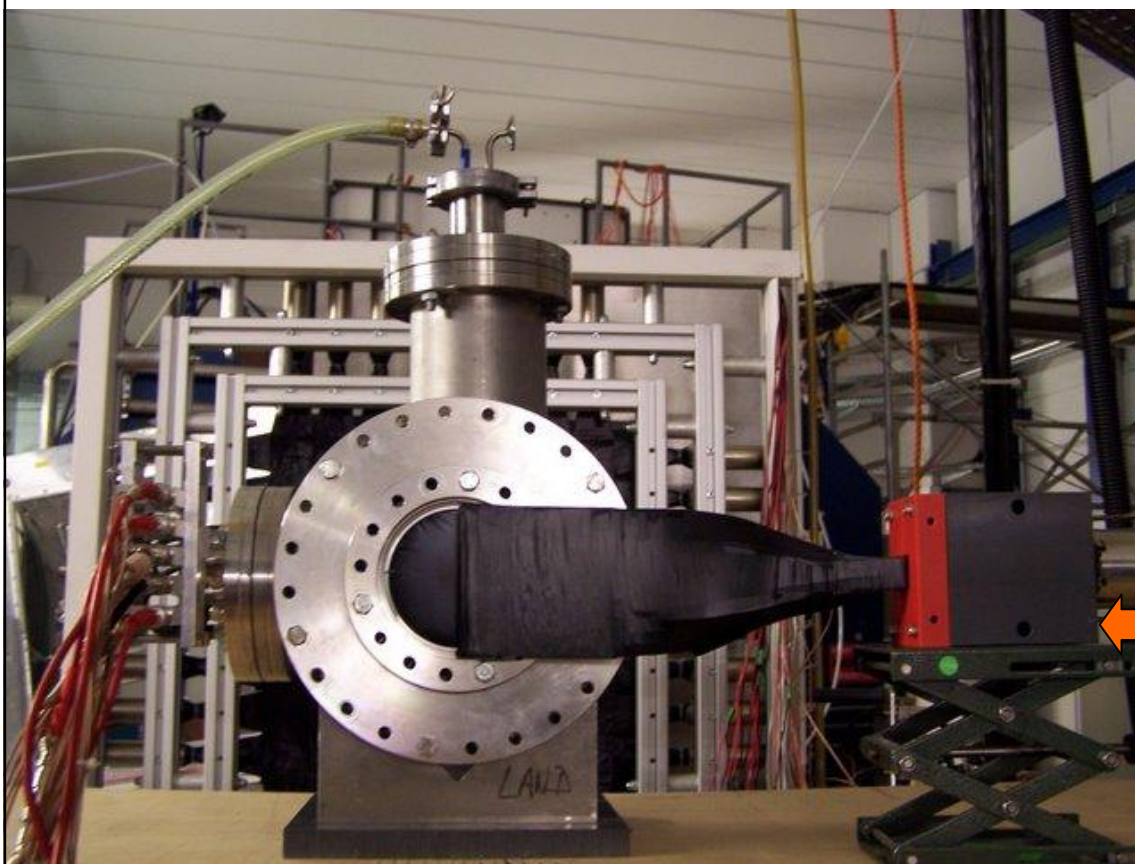


similar: energy loss/position

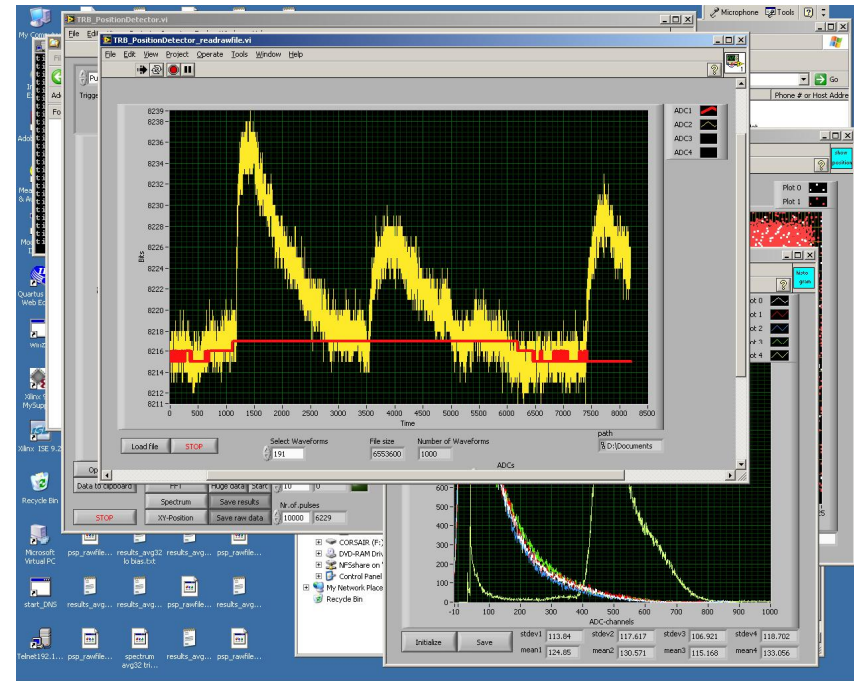
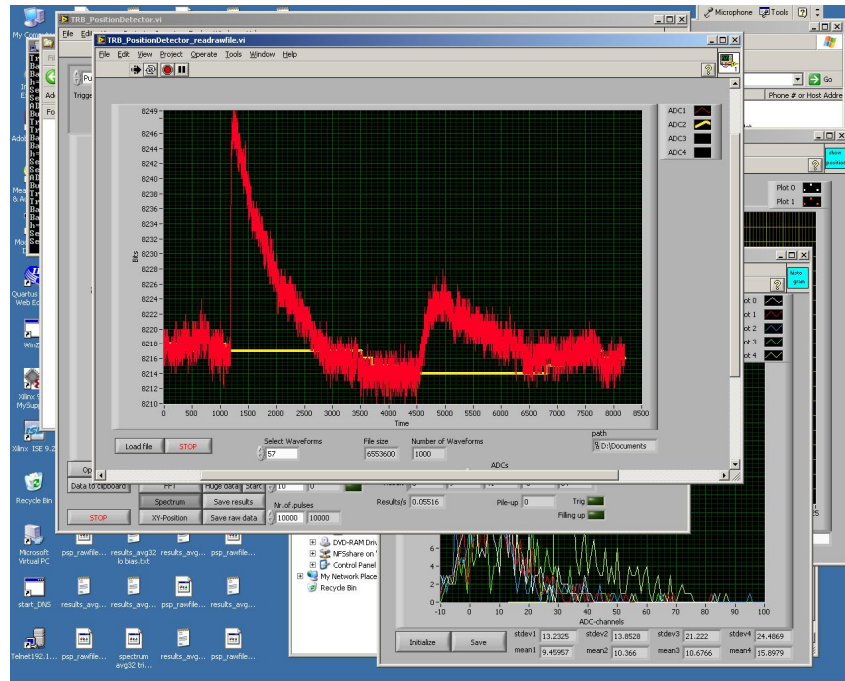
→ fast pos. sens. PIN tracker project (P. Lubberdink, H. Wörtche, H. Simon)

Test experiment S327 (16.-18.4.2008)

^{12}C : 550-700 MeV/u ; 2-50 kEv/s



First Results: Baseline

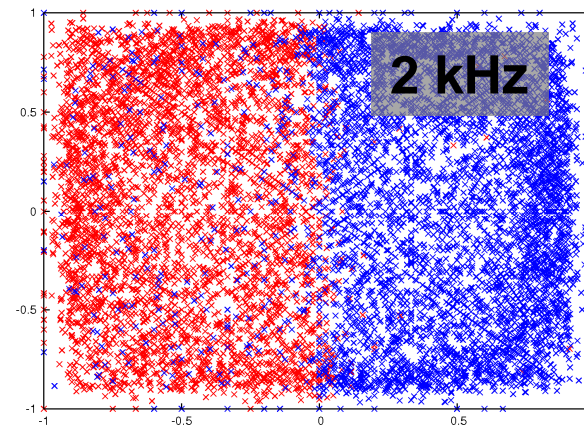
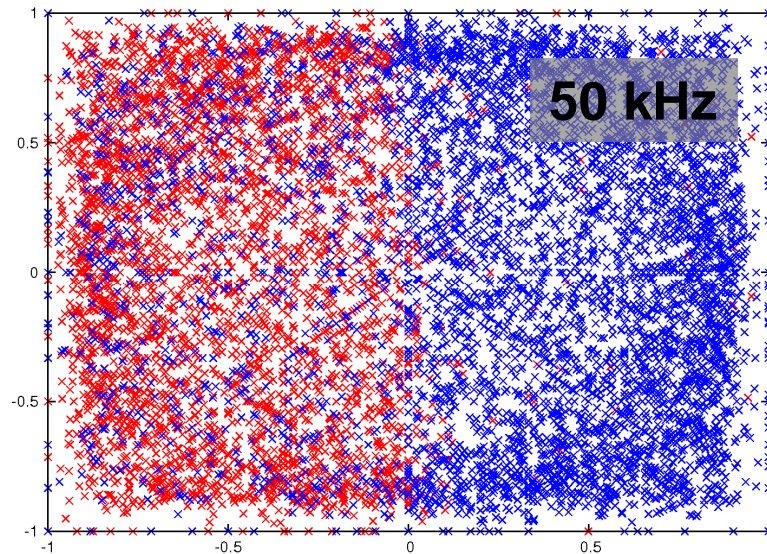


Baseline follower works !
(Bimodal Kalman Filter)

Treatment of double hits !



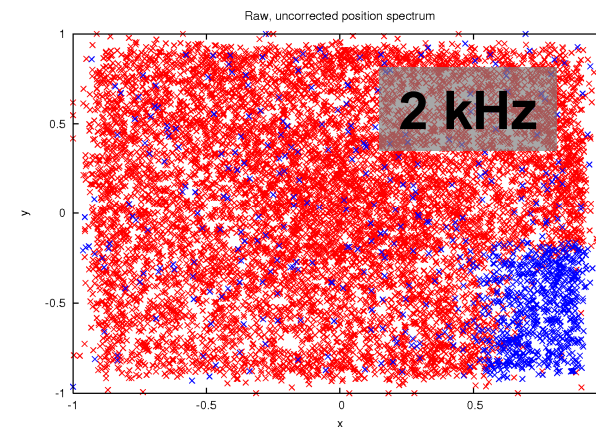
Results: Position



Online reconstruction of positions:

- i. @ full rate (i.e. 50+ kHz, theoretical limit: ADC speed !)
- ii. no correction yet

→ development of a “slow process”



minimal distortions

DAQ Concept for Beam Diagnostics @ FAIR

courtesy
M. Schwickert

FAIR – Accelerator Control System

Responsibility
of Controls

CORBA-based Middleware, e.g. CMW

Responsibility
of Beam
Diagnostics

FESA on
embedded
controller

FESA on
data concentrator (PC)

DAQ module

DAQ module

single board
PC, DSP,
FPGA...

single board
PC, DSP,
FPGA...

Analog signal

Analog signal

Analog signal

Analog signal



Summary

- fast tracking & ID for experiments
 - clean beams required
- most detector systems available (in principle)
→ but better performance would be welcome !
- adaption to Super-FRS needs
- staged implementation
- readout concept → share between
ACC readout FESA/ACS &
EXP readout MBS
- interfaces/Procedures for machine safety develop
- fast sampling and PSA (pile up treatment)



Collaborators

- CU Bratislava
- TU Munich
- JSI Ljubljana
- KVI Groningen
- B. Sitar et al. (MWs)
- R. Gernhäuser et al. (Diamond)
- M. Vencelj et al. (PSA)
- H. Wörtche et al. (FE-Controls)

GSI

- Detector Laboratory
- Experimental Electronics
- Accelerator Group
- Ch. Schmidt et al.
- E. Badura et al.
- R. Bär, P. Forck, et al.

Eol: Helsinki University (Rad-hard Si/GEM)



ID Detectors at the Super-FRS

beam optics → $\left\{ \begin{array}{l} \text{size (400x60) mm}^2 \\ \text{position resolution } \sim 1\text{mm} \end{array} \right.$

Fluorescent Screen		
Number of elements		1
Overall length	mm	50
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Specs ff

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Capacitive Pick-up

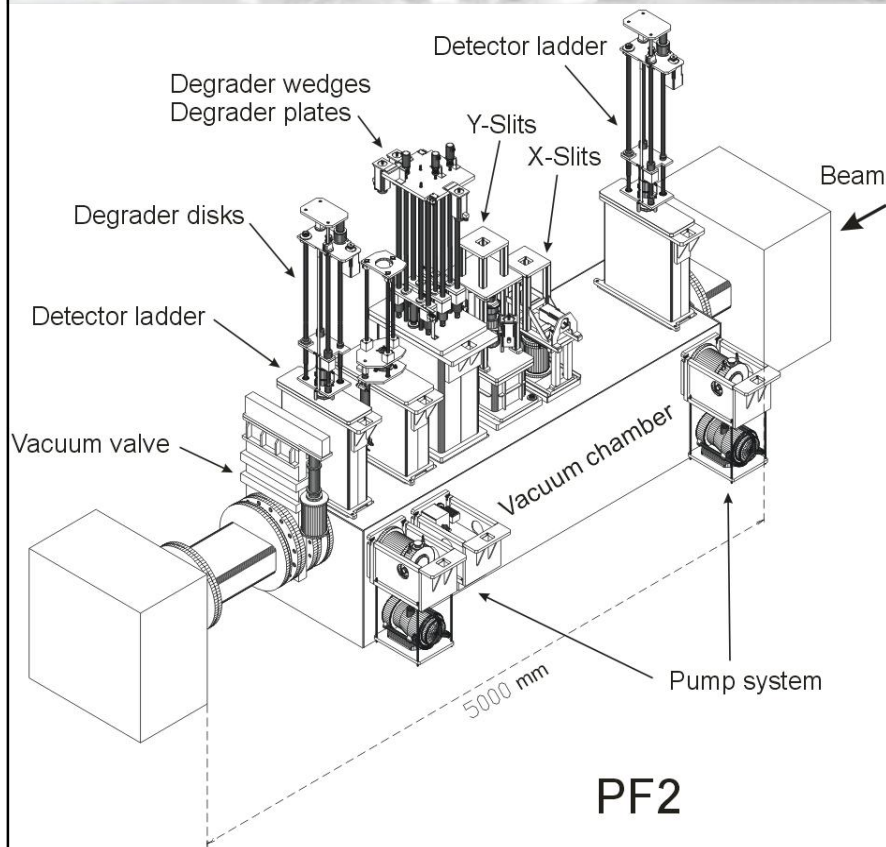
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Specs ff.

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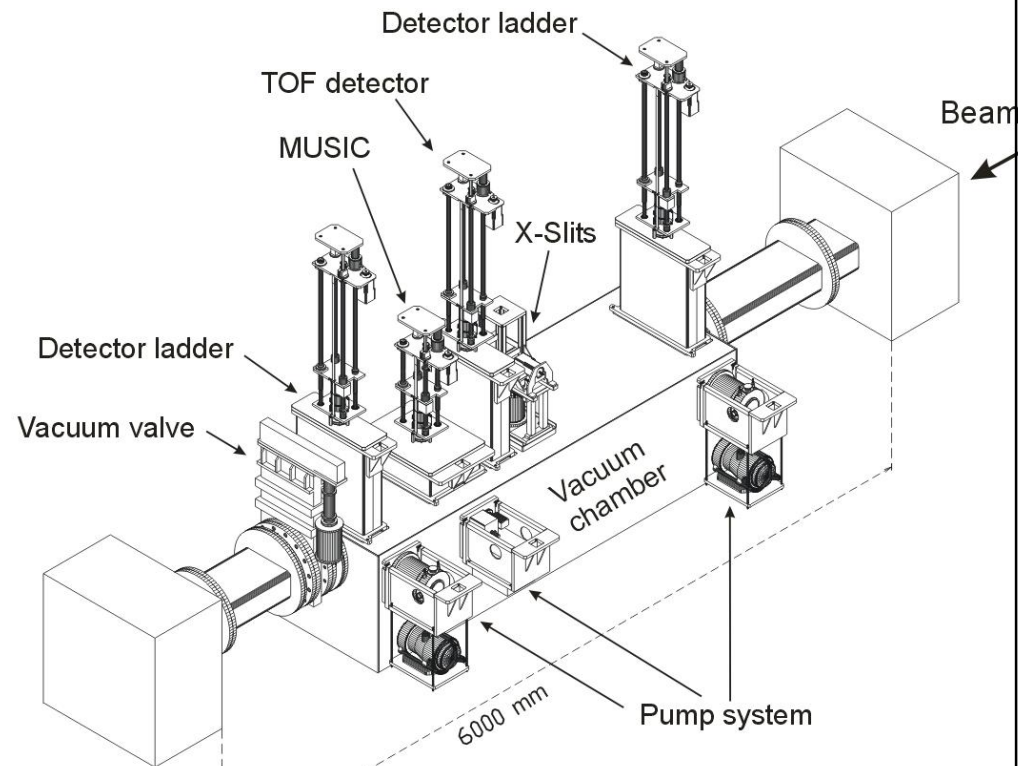
ToF (diamond detectors, PC-CVD-DD)		
Number of elements		4
Overall length	mm	200
Horizontal aperture	mm	400
Vertical aperture	mm	50
Pitch	mm	1
Time resolution	ps	<50

Diagnostic boxes designs

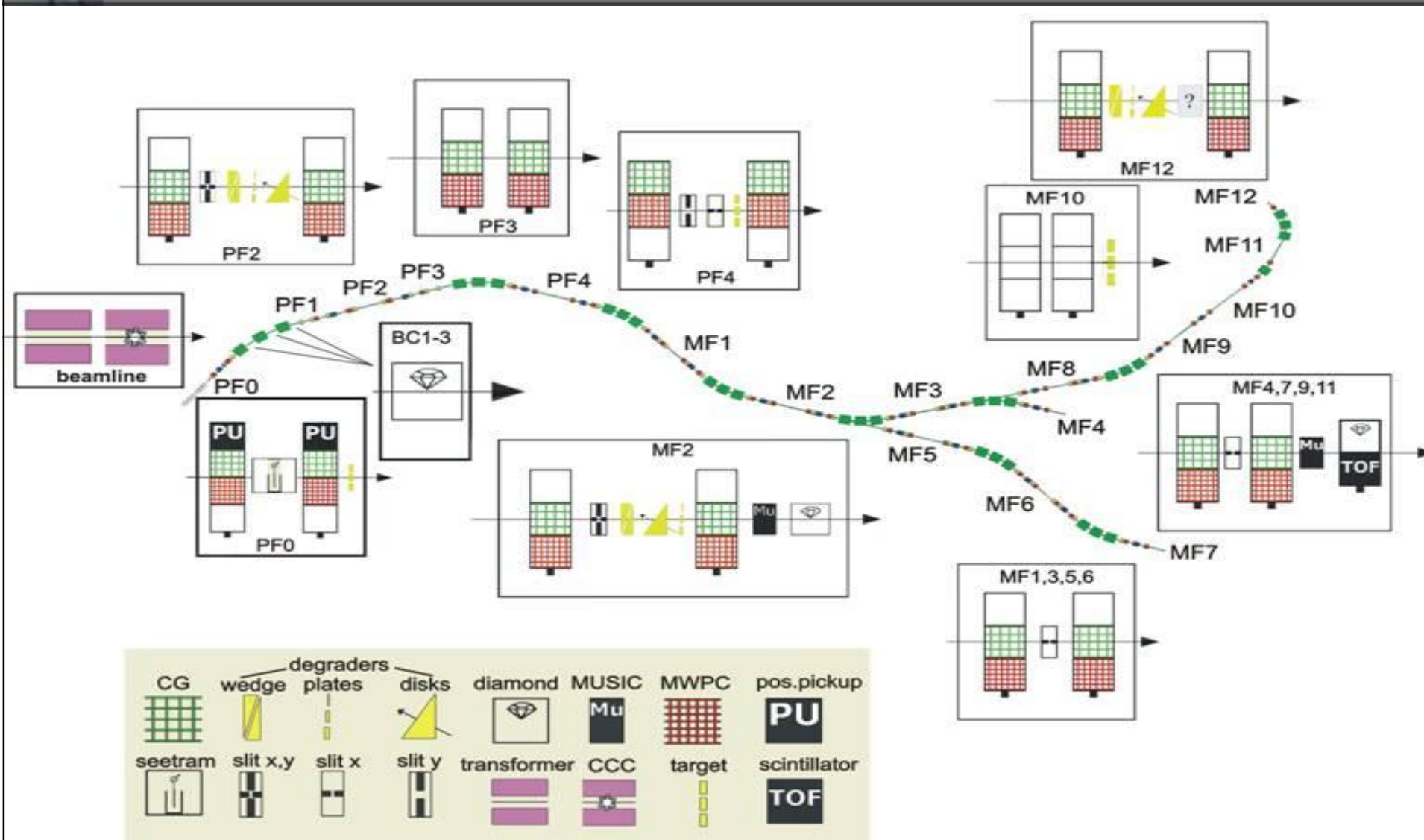


Beam diagnosis

Beam diagnosis & experiments



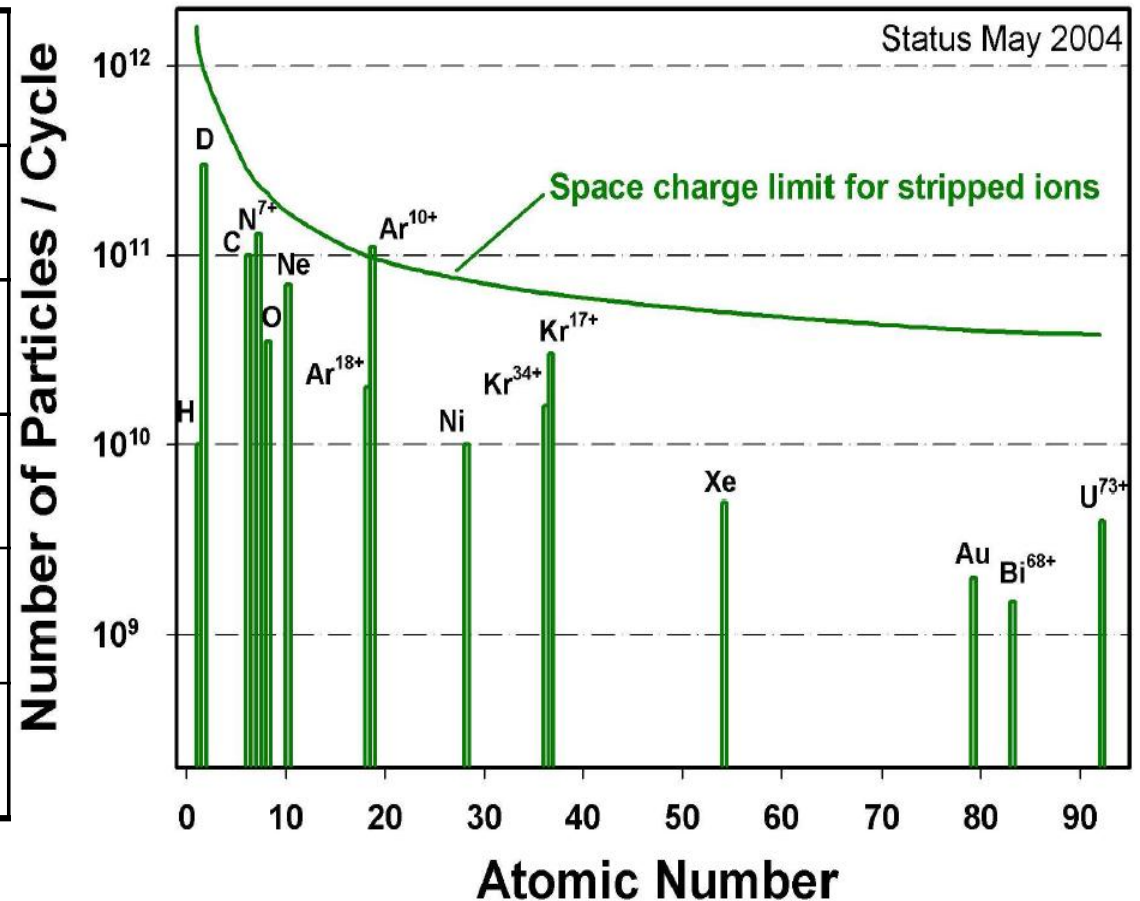
MF4



Staged implementation

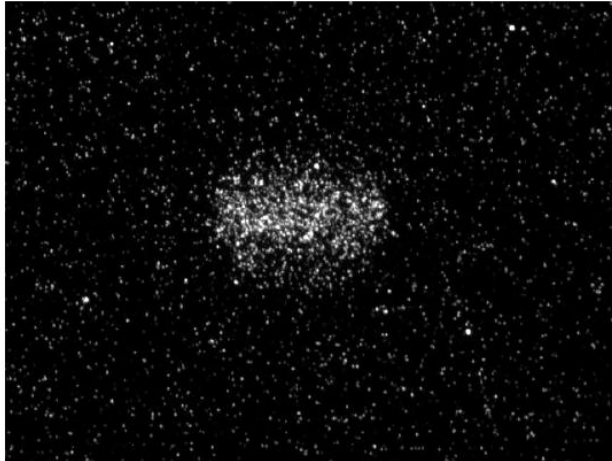
R&D high current admissible for some years!

Fair Stage	today	0 (Existing Facility)
Ion Species	U ⁷³⁺	U ⁷³⁺
Maximum Energy	1 GeV/u	1 GeV/u
Maximum Intensity	3x10 ⁹	2x10 ¹⁰
Repetition Rate	0.3 Hz	1 Hz
Approx. Year		2008/2009

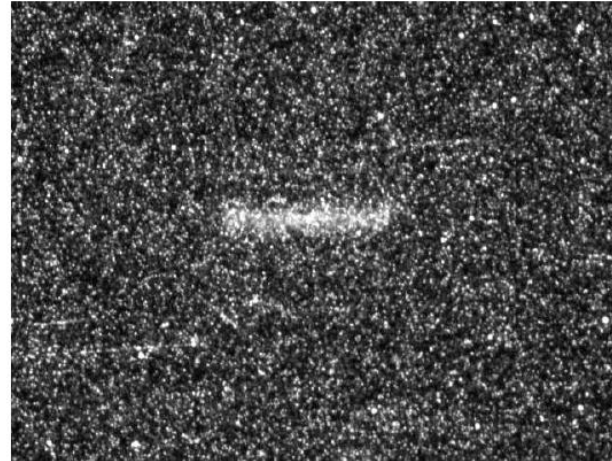


Applications to beam transport line

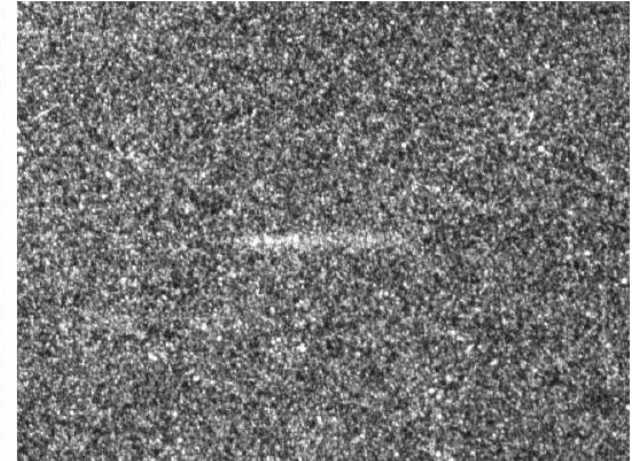
P. Forck F. Becker



(a) $4 \cdot 10^8$ U @ 60 MeV/u



(b) $1,2 \cdot 10^8$ U @ 350 MeV/u

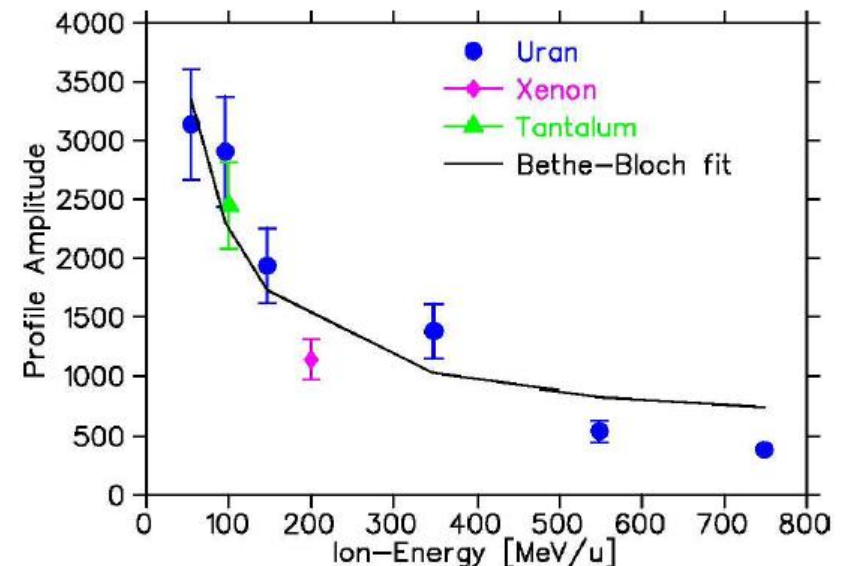


(c) $1 \cdot 10^9$ U @ 750 MeV/u

S/N gets worse

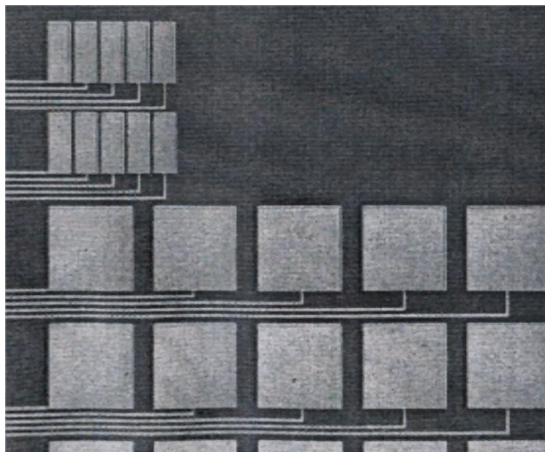
- energy loss
- neutron background in the imaging system

→ R&D fiber/mirror optics UV
~390nm



Segmented CVDD detectors

RD42 collaboration



Pixel readout by Si_3N_4 - isolated micro tracks.

Pixels (110 x 290) μm^2 , (400 x 400) μm^2 .

Tracks 15 μm / pitch 30 μm

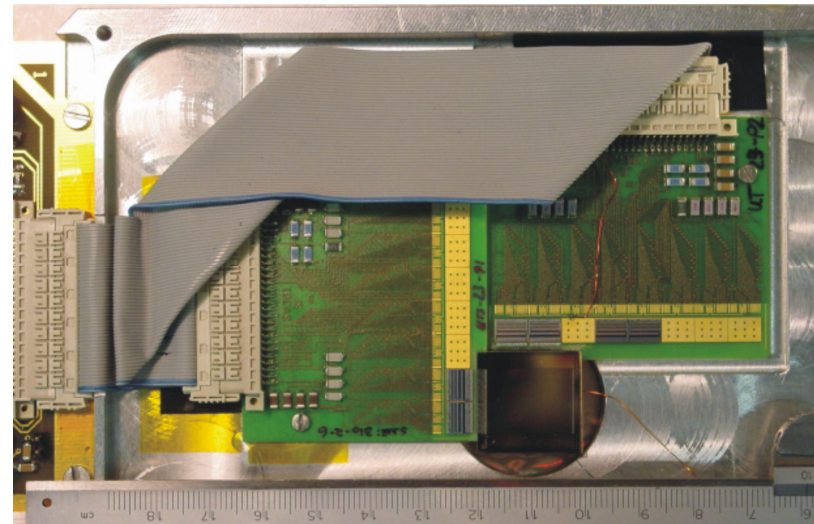
CVDD micro strip 50 μm

APV-25 CERN/CMS ASIC

shaping time: 25 ns

analogue daisy chains

128 channels per chip



Diamond detector test

Radiation Hardness:

some samples show persistent photo current (PPC) after irradiation limit

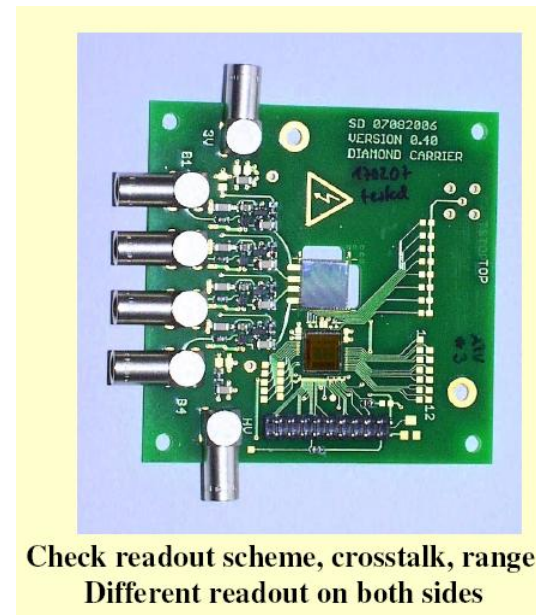
$^{16}\text{O}@112\text{MeV } 10^{13} \text{ cm}^{-2}$

Efficiency: **98% for $^{16}\text{O}@120\text{MeV}$**

Signal Properties:

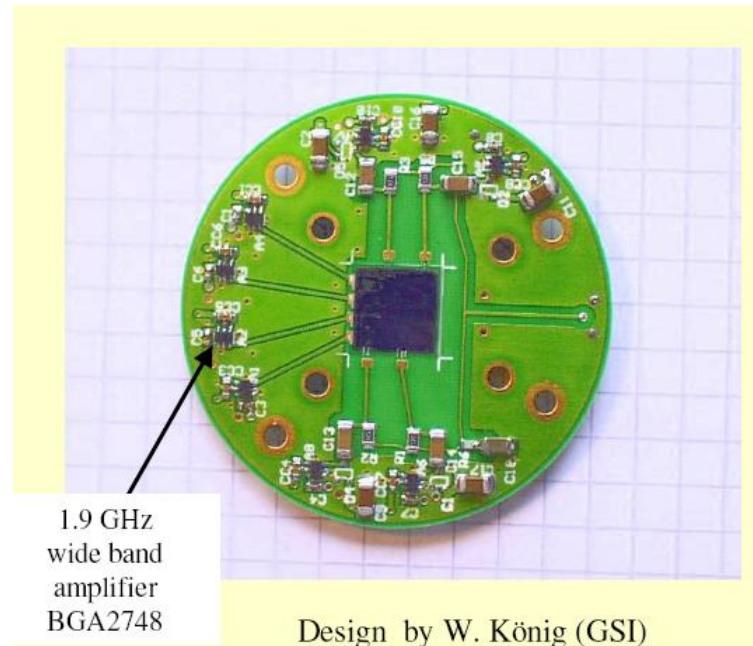
new frontend electronics based on the APV25 chip (CMS tracker chip) produced and in use

PC CVDD (10x10) mm²



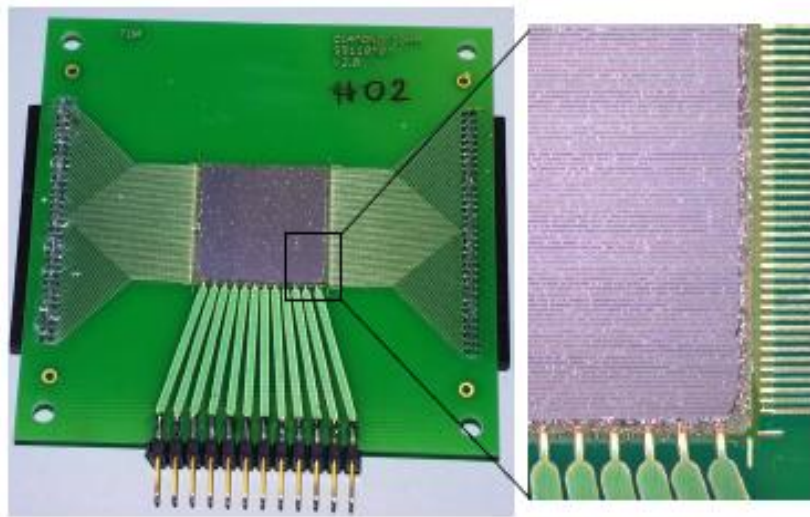
Fast pre-amplifiers

- New low power pre-amplifier from HADES Start detector



- New pre-amplifiers for HISPEC/DESPEC –LYCCA designed by R. Schneider (Mesytec)

Diamond Detector for R³B

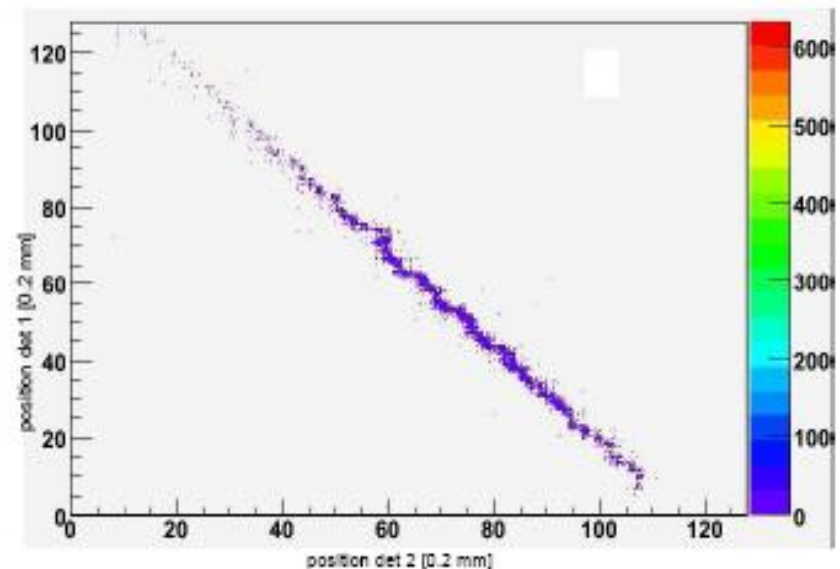


- 2.54x2.54 cm²
- 200 μm pitch, 20 μm gap
- Back side divided in 16 Al strips each with a gap of 50 μm
- excessive cost

¹²⁹Xe @600 MeV/u, 10⁵ pps

Tested with ¹²C beam

R. Gernhäuser (TU-München) *et al.*

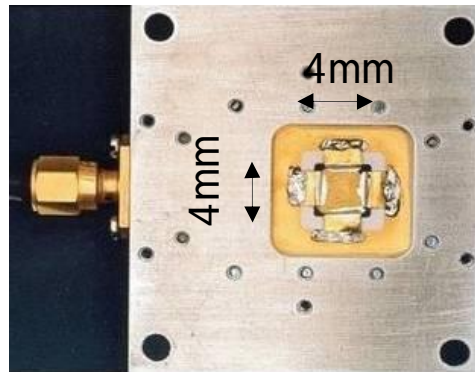


Diamond Detector test

SC CVDD

Tested at CNA-Seville

$p, \alpha, {}^7\text{Li}$ low energy beams



- Energy resolution $\sim 1\%$ (similar to Si)
- $\Delta t < 100\text{ps}$
- Estimated efficiency $> 70\%$
- Samples irradiated up to 10^9 ions/s cm^2

SC CVDD , 110-500 μm
(GSI Detector Laboratory)

Diamond detector test

Radiation Hardness:

some samples show persistent photo current (PPC) after irradiation limit

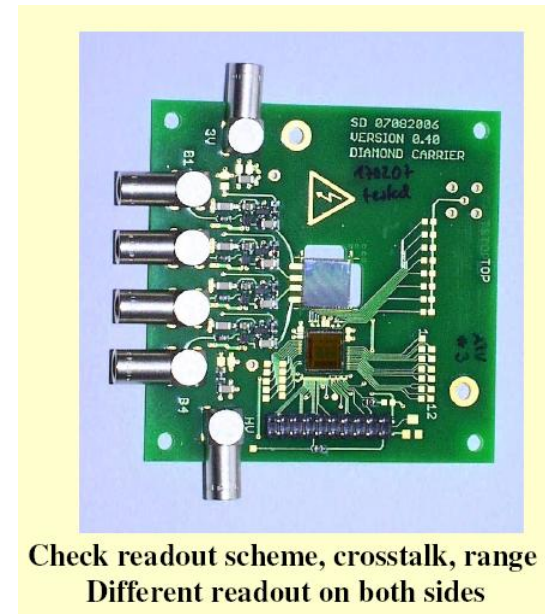
$^{16}\text{O}@112\text{MeV } 10^{13} \text{ cm}^{-2}$

Efficiency: **98% for $^{16}\text{O}@120\text{MeV}$**

Signal Properties:

new frontend electronics based on the APV25 chip (CMS tracker chip) produced and in use

PC CVDD (10x10) mm²

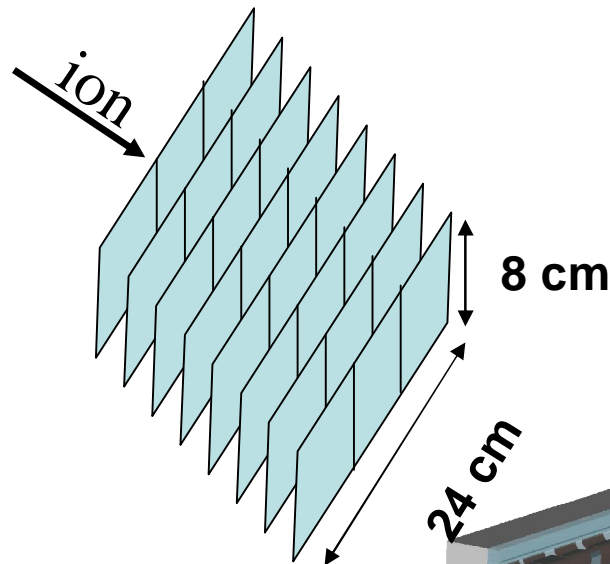
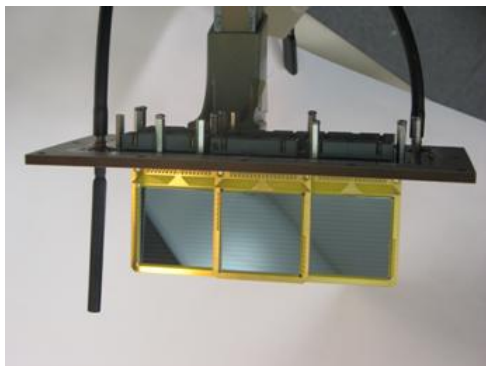


R. Gernhäuser (TU-München) *et al.*

HISPEC/DESPEC Advanced Implantation Detector Array (AIDA)

Up to 10 planes of

RISING active stopper



- DSSSD, 8 x 8cm, d=1 mm
- pitch 625 μ m, 128 x 128 strips
- ΔE (FWHM)~ 10 keV
- Δt (FWHM)~1 ns
- threshold < 50 keV

Observe:

$p, 2p, \alpha, \beta, \gamma, \beta p, \beta n \dots$ decays

Edinburgh

T. Davinson, P. Woods *et al.*

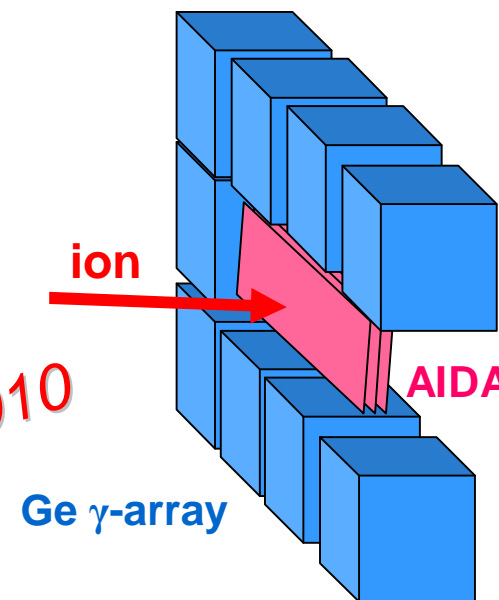
Liverpool:

R. Page *et al.*

STFC DL & RAL:

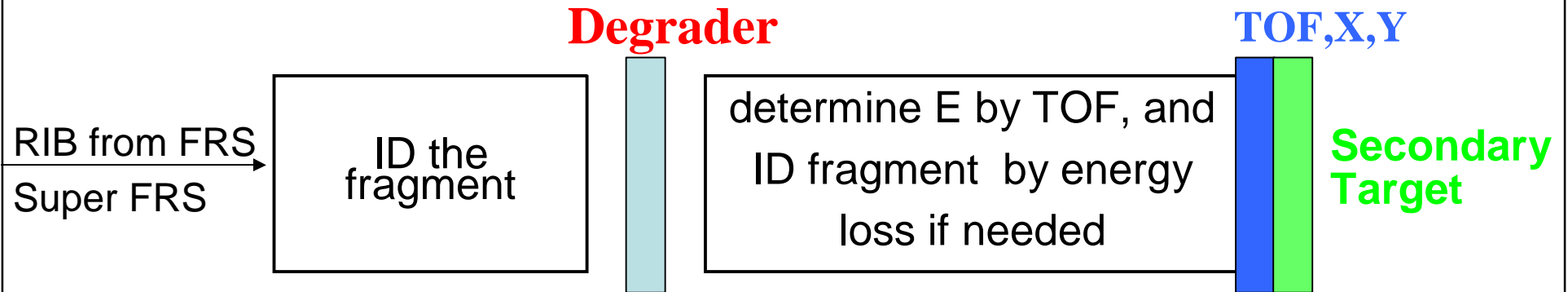
J. Simpson *et al.*

Ready in 2010

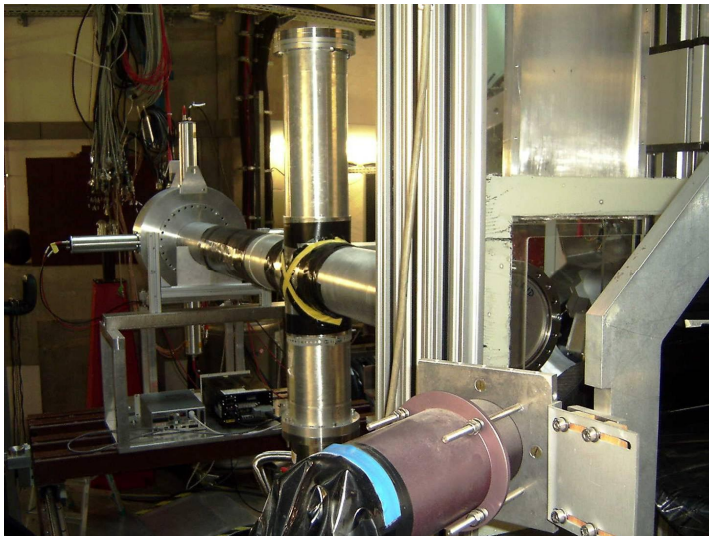


T. Davinson (Edinburgh)

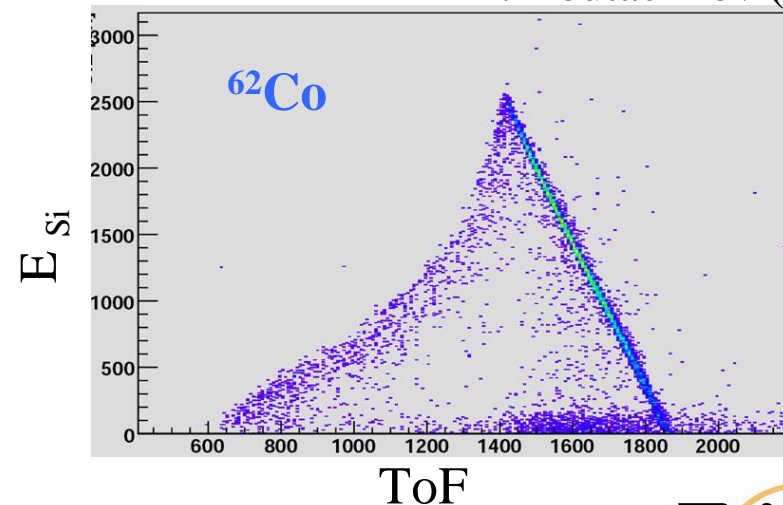
Test detector for slow down beams



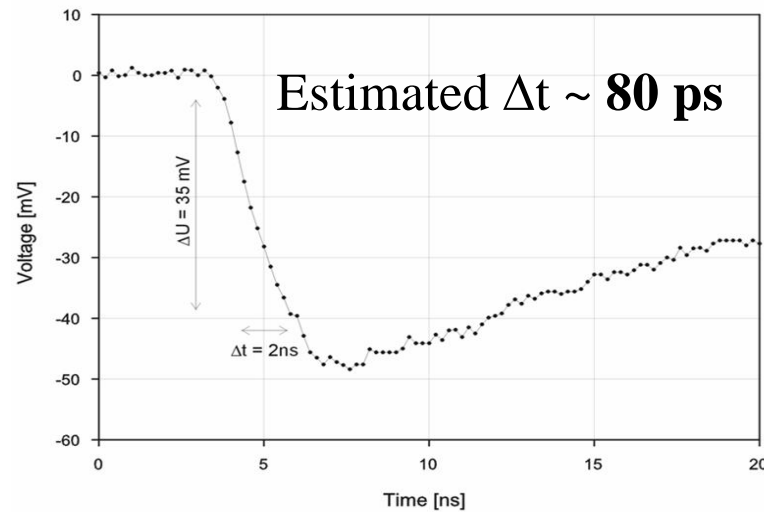
Tof: $\Delta t < 150 \text{ ps}$, rate: 10^7 pps



P. Boutachkov (GSI)



Test detector for slow down beams



GSI group:

P. Boutachkov, M.Górska,
J.Gerl, H.Geissel, W.Koenig
I.Kojouharov, C.Nociforo,
W.Prokopowicz,
H.Schaffner, H.Weick

LNL group:

J.J.Valiente, A.Gadea

JINR Dubna:

N.Kondratiev

Large Area Secondary Electron Detection

1.5x1.0x1.0 m³



Sevilla group:

J.Gomez Camacho,
M.Alvarez, J.M.Espino,
I.Mukha, J.M.Quesada

Development with
MCP, MICROMEGAS
technology

Saclay:

A.Drouart, A.Polacco

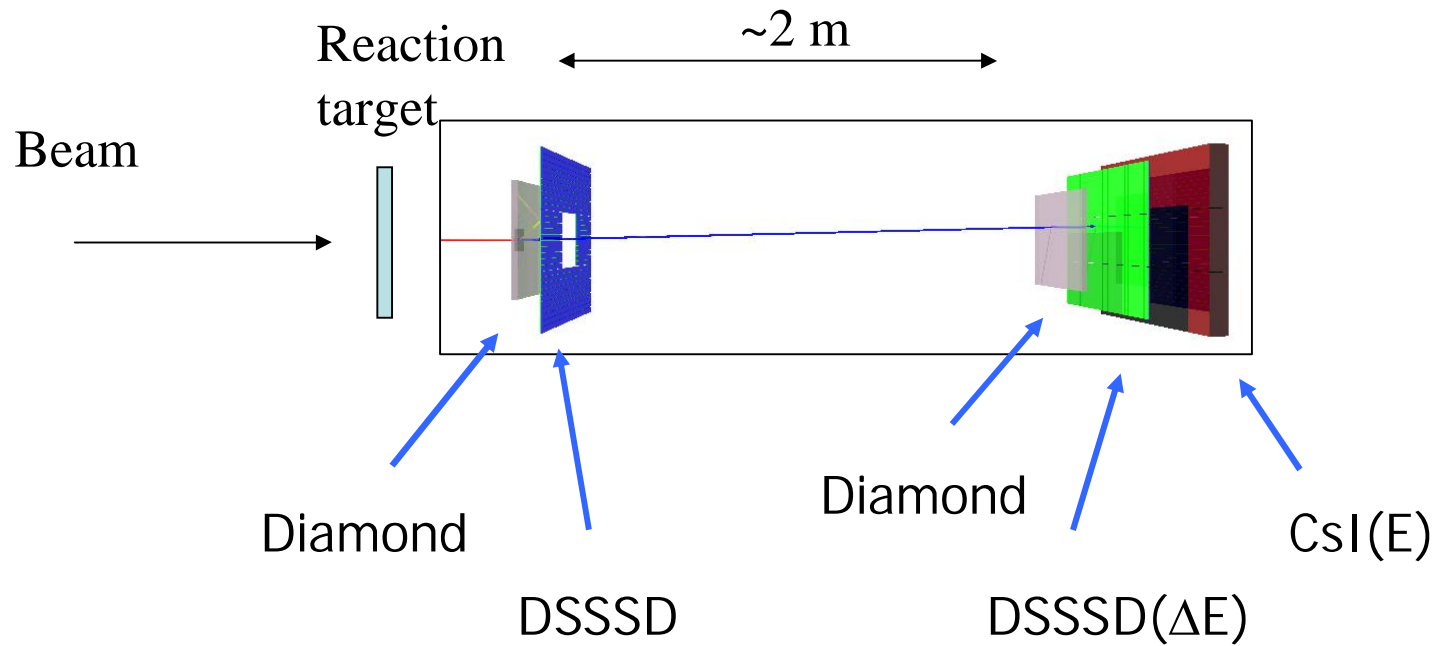
Koln:

J.Jolie, F.Naqvi, C.Pascaoui

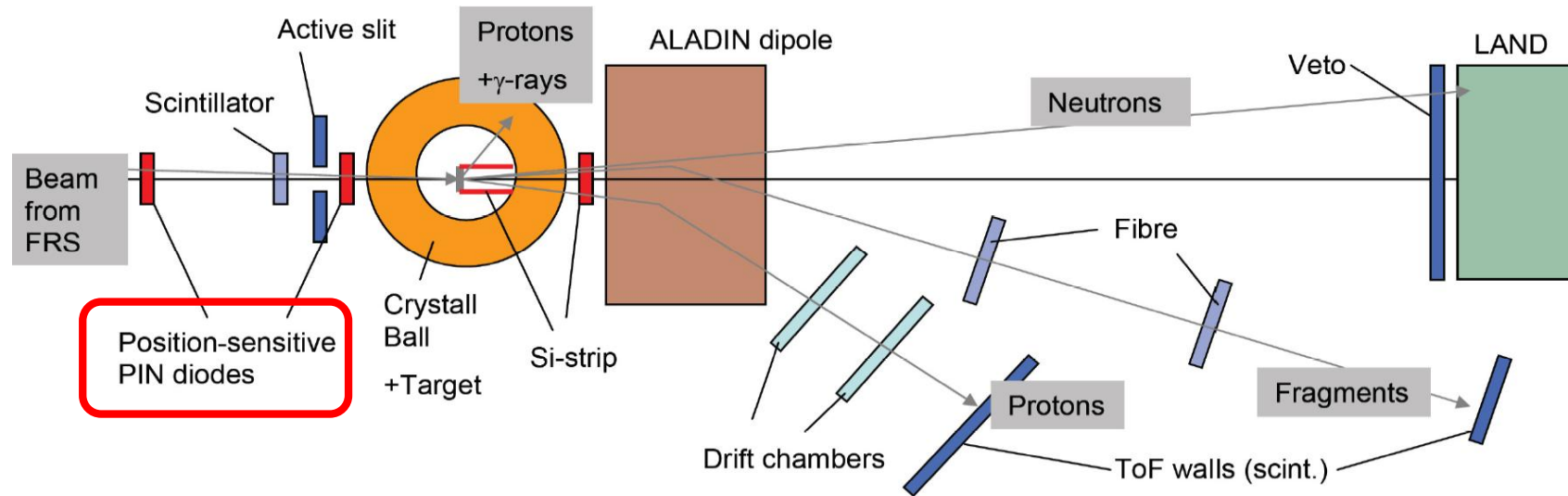
HISPEC/DESPEC

Lund-York-Cologne CALorimeter

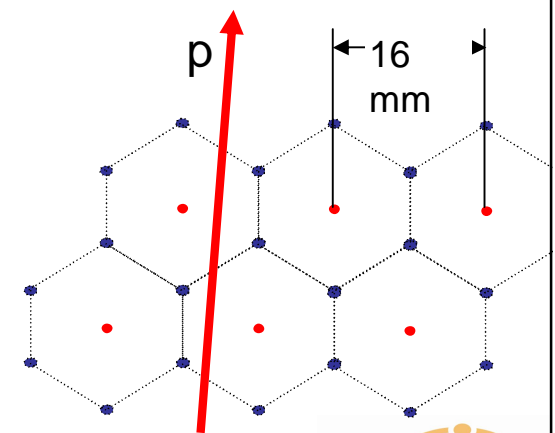
(LYCCA)



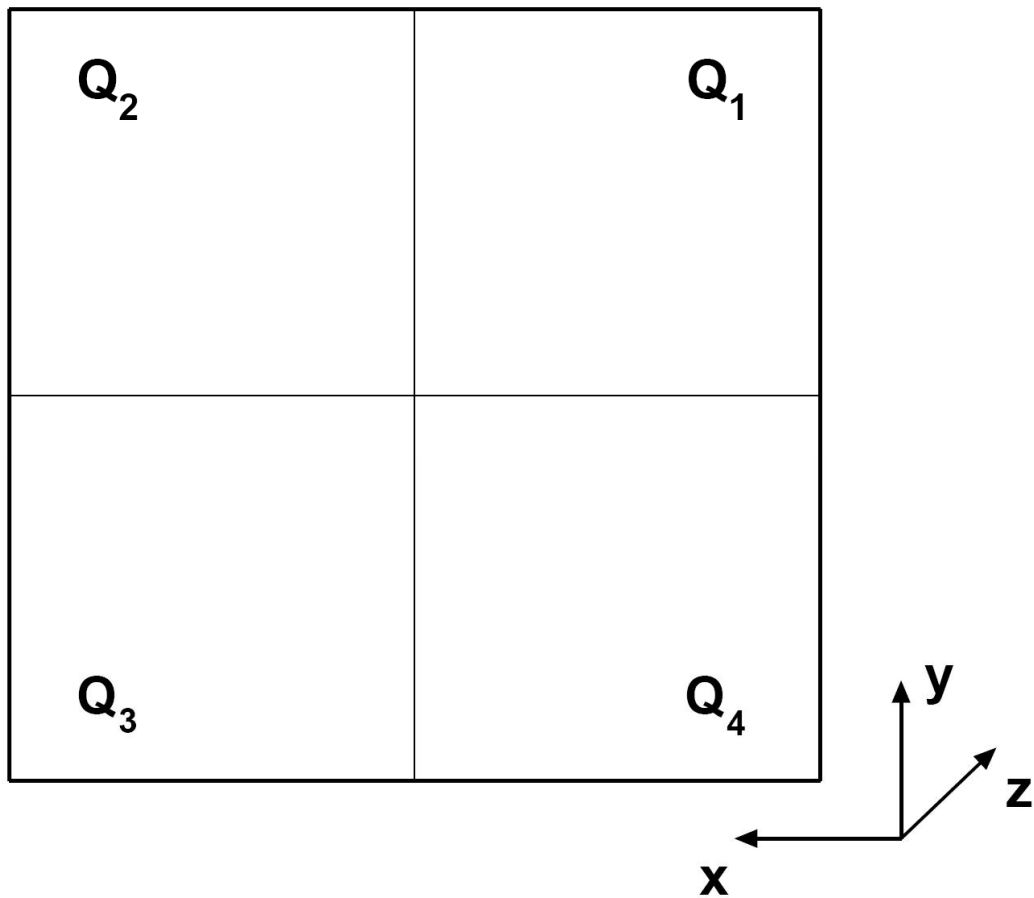
Prologue: Extended experimental Setup at Cave C



300 μm high n-type Si
 4,5 x 4,5 cm^2
 B doped \rightarrow p-side



PSP



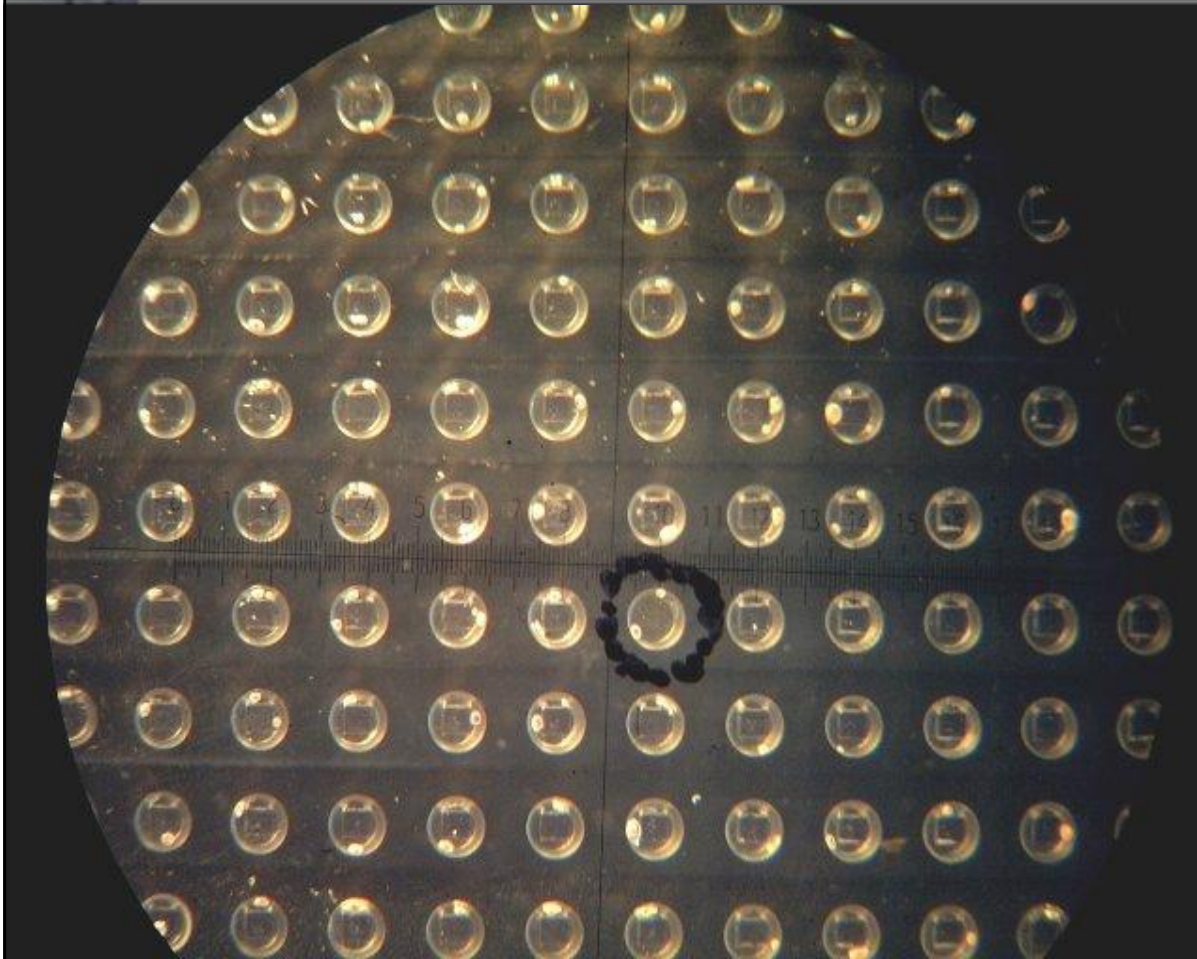
- Cathode : Sum energy
- 4 Anodes \rightarrow position

$$u = (Q_2 + Q_3) - (Q_1 + Q_4) / Q$$
$$v = (Q_1 + Q_2) - (Q_3 + Q_4) / Q$$

$$Q = Q_1 + Q_2 + Q_3 + Q_4$$

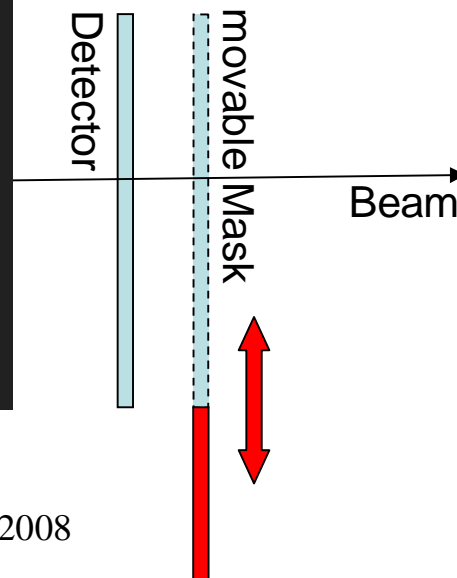
$$\rightarrow x(u,v) ; y(u,v)$$

PSP



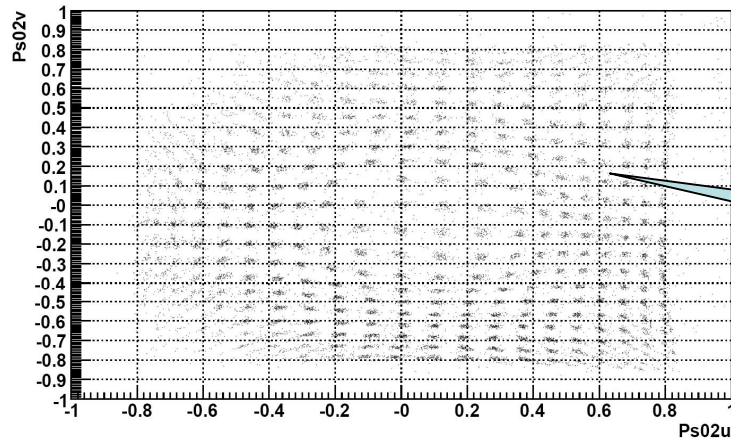
Calibration via active mask

i.e. Scintillator dots glued into PMT read out light guide



PSP

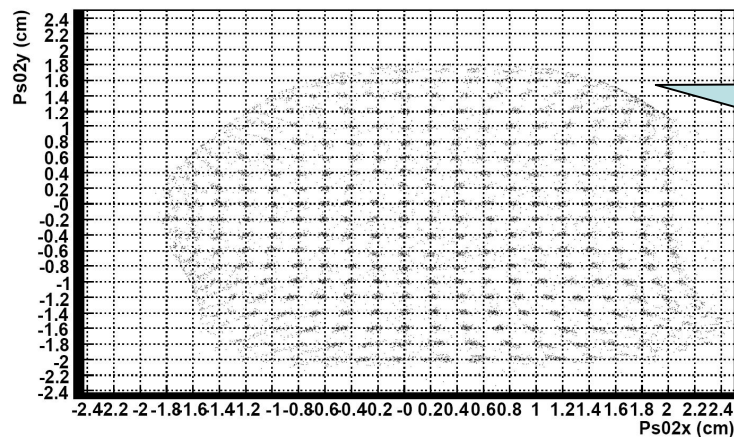
PSP2 position reconstruction before position calibrations (DHIT data)



≡ Semi automatized
Off- or Near line calibration

Distortions
Gain Matching
Automatic pedestals from DAQ system

PSP2 position reconstruction after position calibration (HIT data)

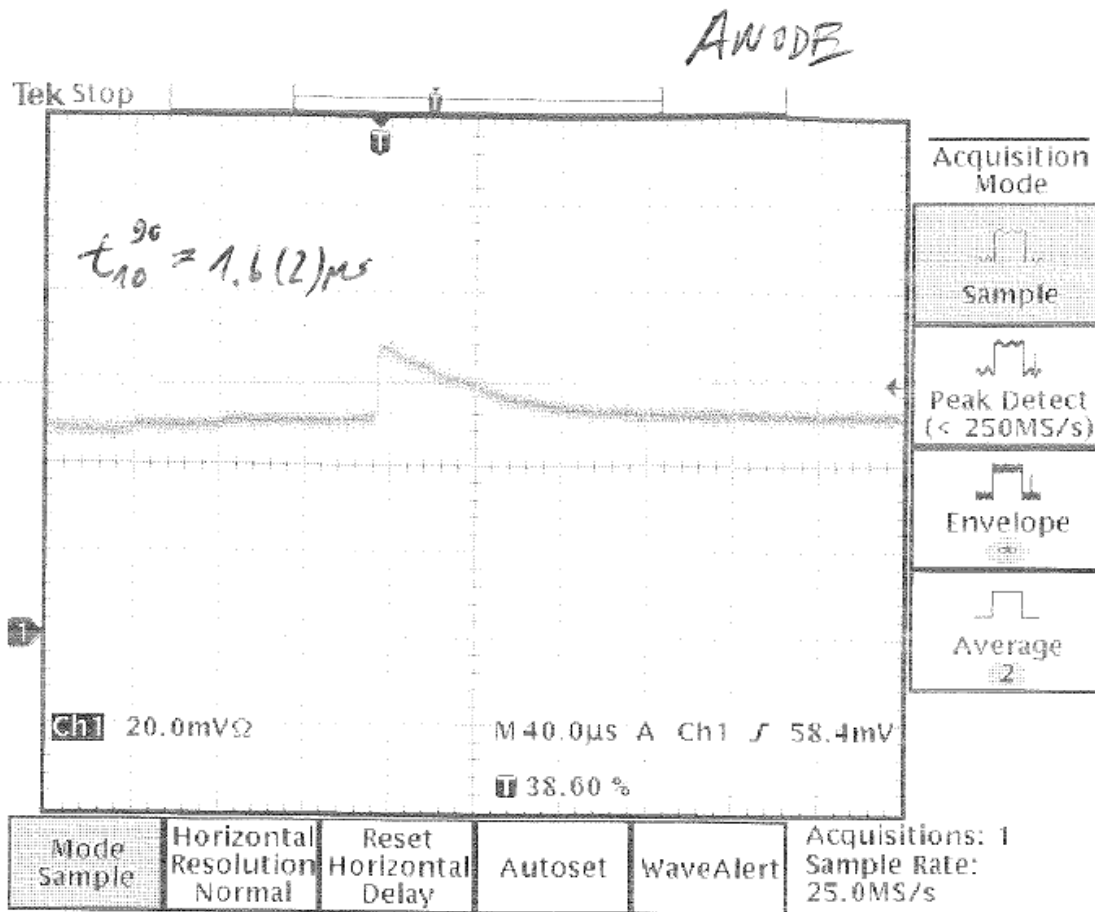


Threshold
effect

**Rate limit to few kHz
(conv. readout chain)**

**Need:
intelligent fast sensor !**

PSP



Signal characteristics:

- few mV
- preamp decay time
~few 10 μs
- riding on noise and ripple

→ pile up treatment
→ good thresholds

Idea: Use ADC coupled to Hades TRB2 of KVI: Peter Schakel / Pim Lubberdink

- Available hard/software environment:

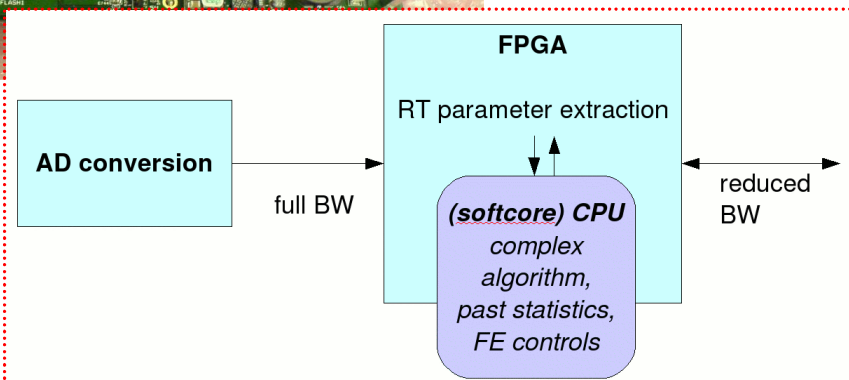
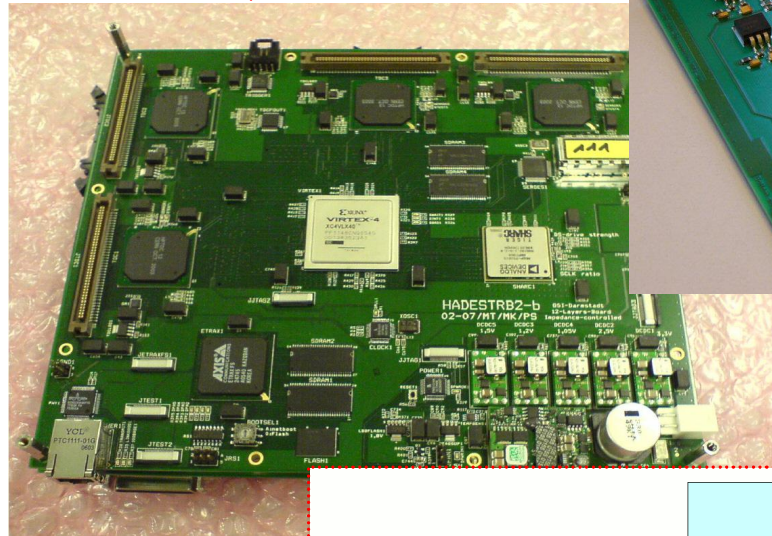
(1) ADC Piggy back / KVI
100MS/14Bit
50MHZ BW

(2) Xilinx based board
HADES TRB2

(3) Base line follower/
 $k\sigma$ trigger

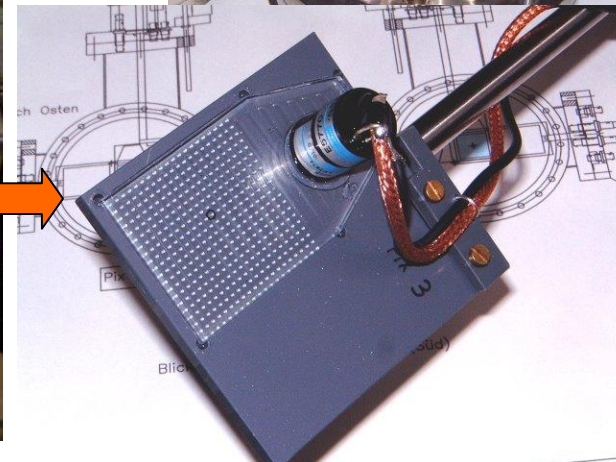
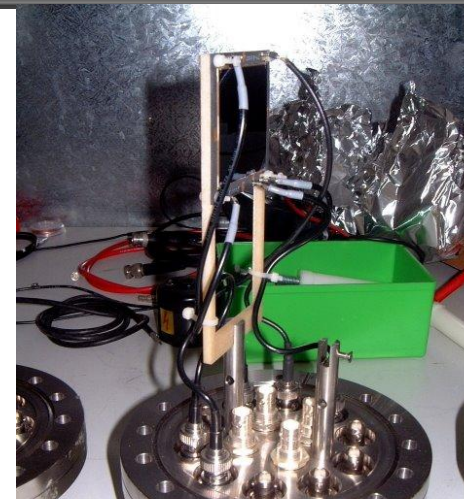
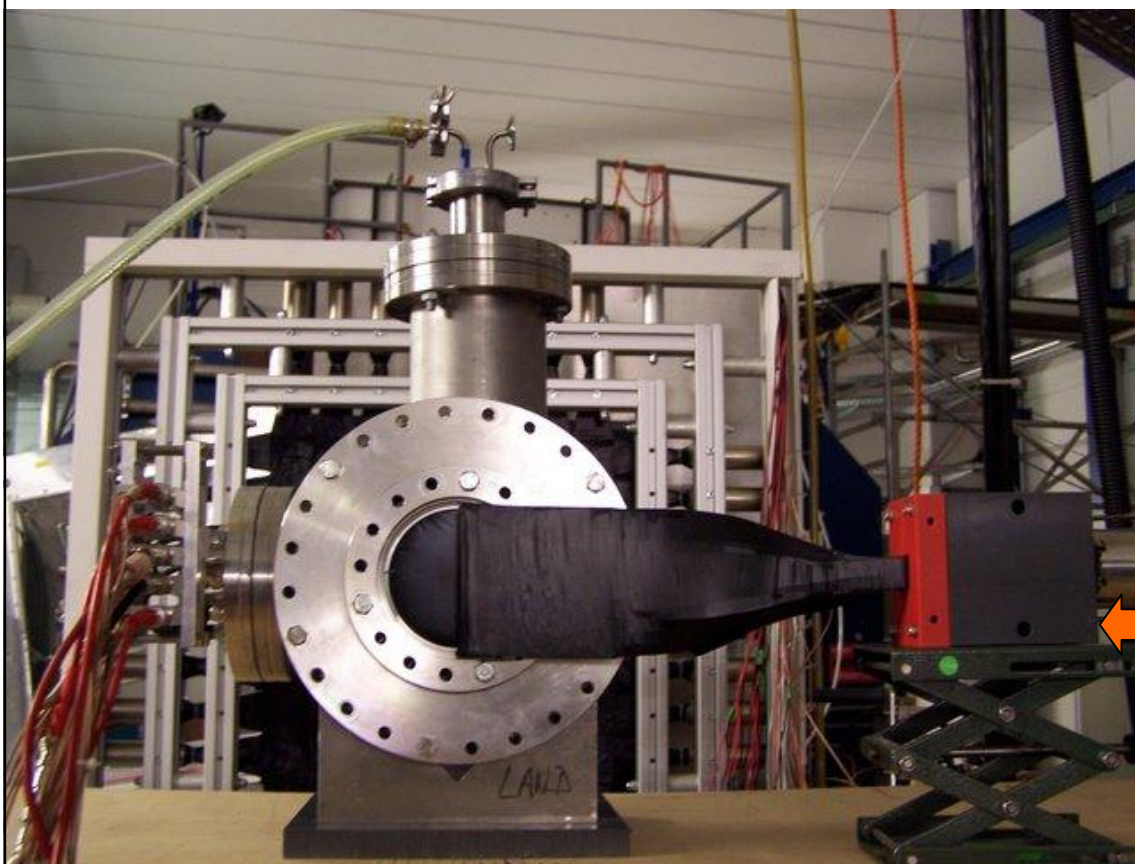
(J. Jungmann / M. Vencelj)

Labview based readout system

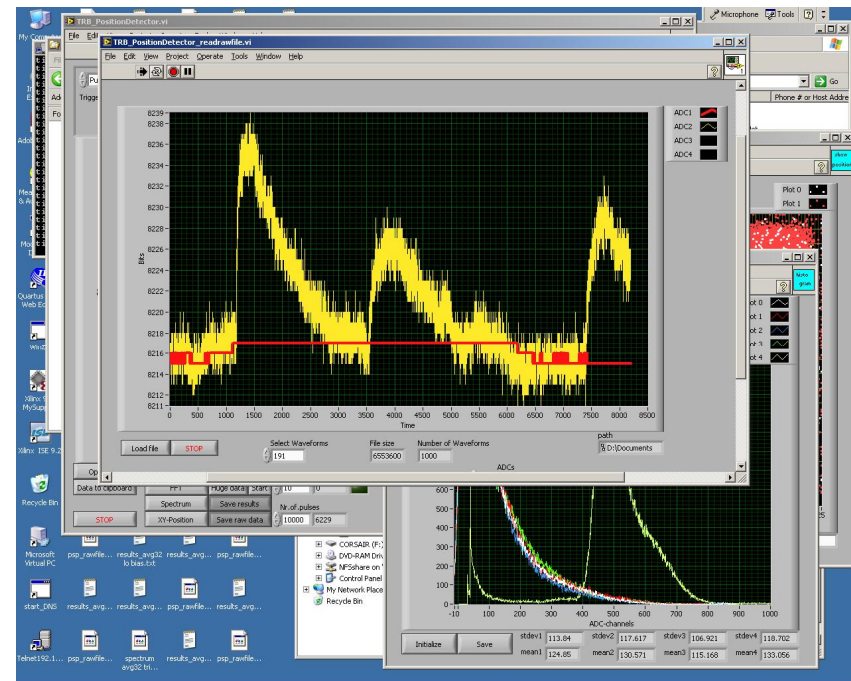
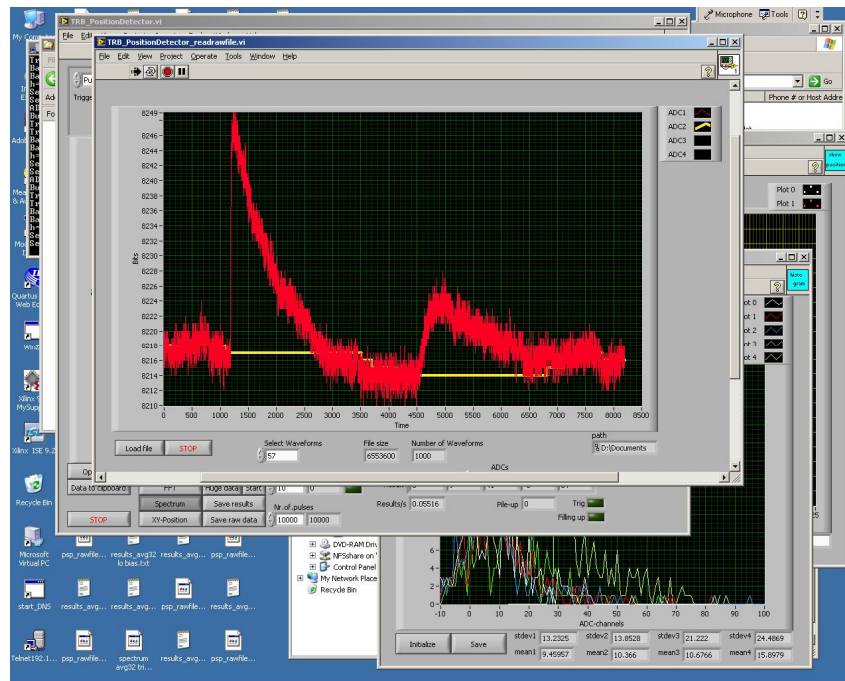


Test experiment S327 (16.-18.4.2008)

^{12}C : 550-700 MeV/u ; 2-50 kEv/s



Results: Baseline

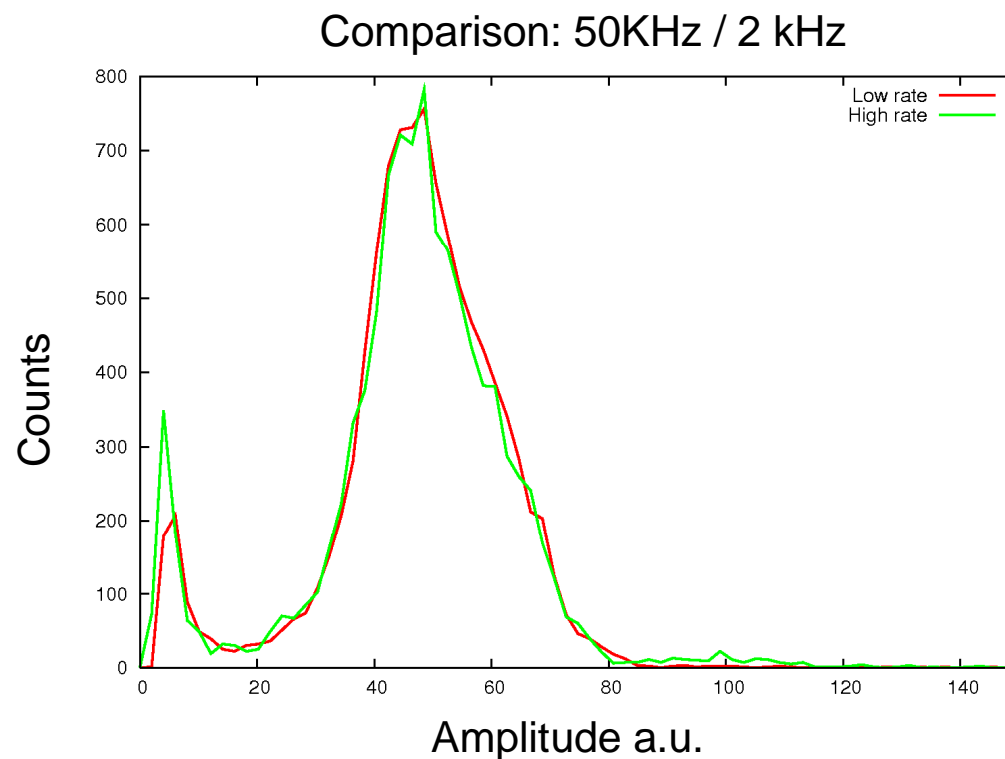
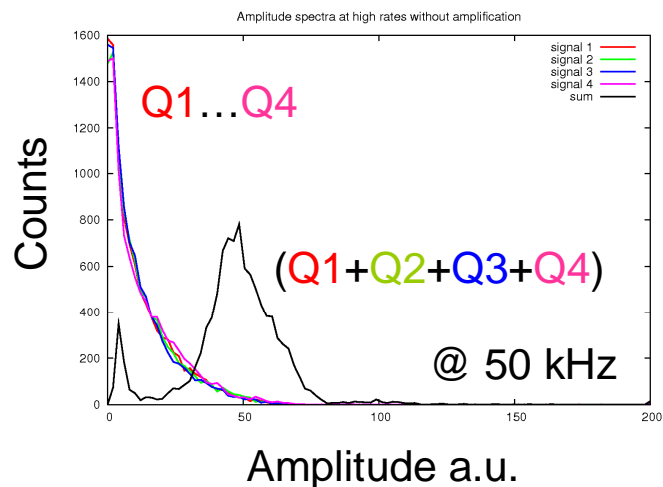
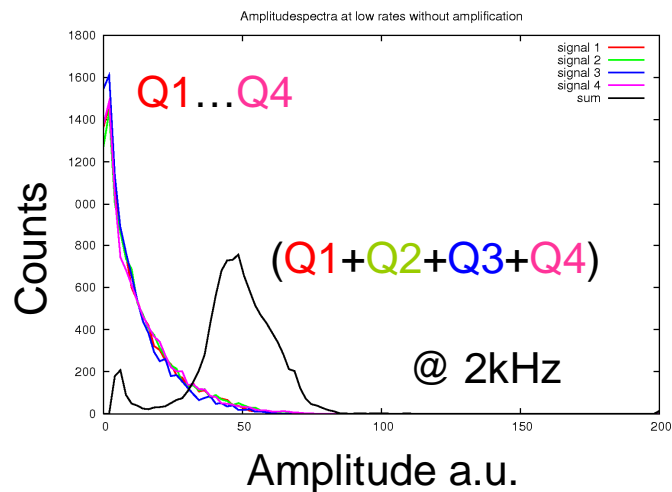


Baseline follower works !
(Bimodal Kalman Filter)

Treatment of double hits !

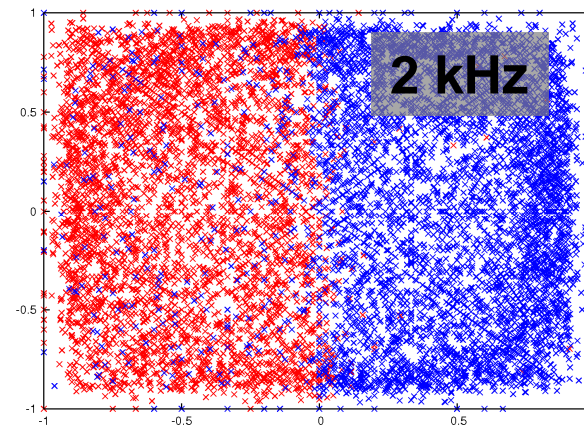
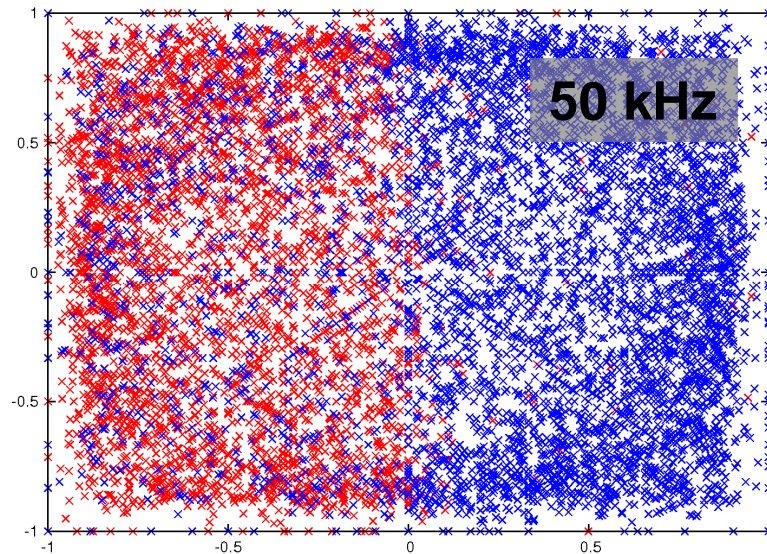


Results: Amplitude $\rightarrow \Delta E$



Gain matched amplitude spectra
No degradation with rate !

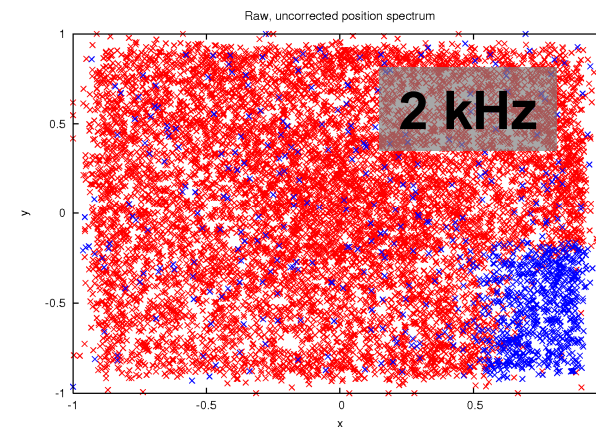
Results: Position



Online reconstruction of positions:

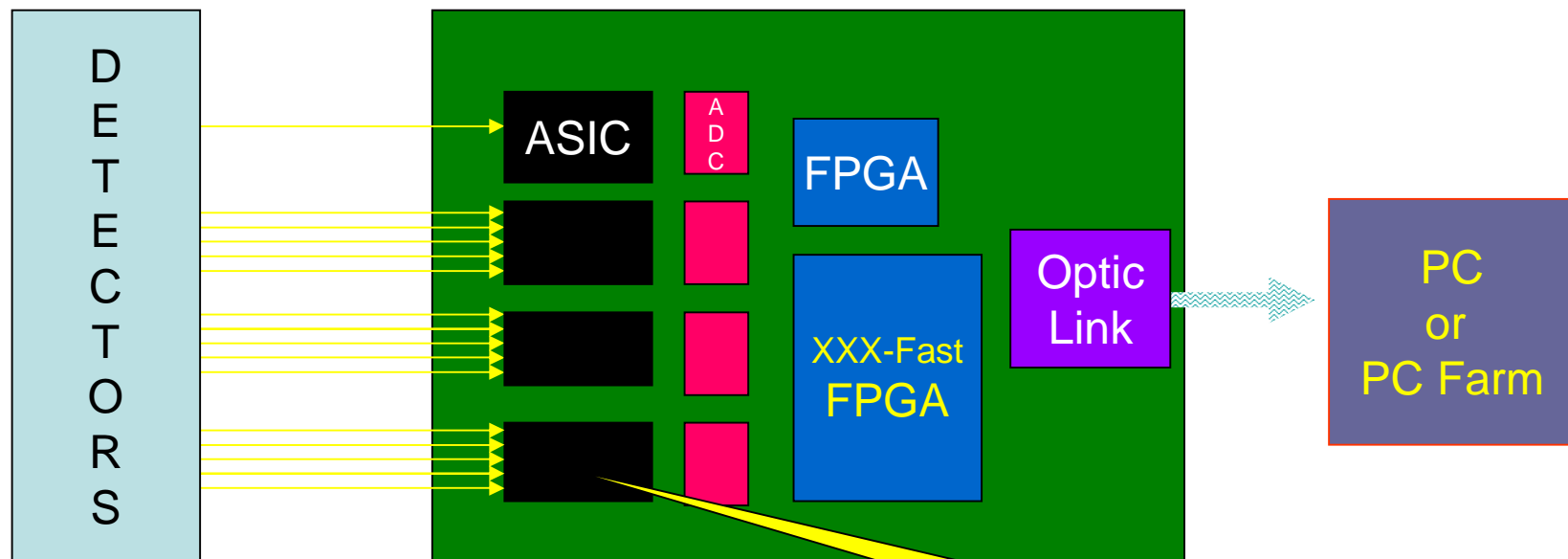
- i. @ full rate (i.e. 50+ kHz, theoretical limit: ADC speed !)
- ii. no correction yet

→ development of a “slow process”



minimal distortions

... so what do we really want (c.f. FREEDAQ proposal)



PMT, APD, PD (γ , n, cp)
Si(Li), DSSD, IC (cp: highly segmented devices)
TPC(GEM, Micromegas, ...),

Pulse height, Q integration
Time
Pulse shape



Collaboration

KVI

H. Wörtche, J. Jungmann,
P. Lubberdink, P. Schakel,
V. Stoica

TU Darmstadt
D. Savran, B. Löher

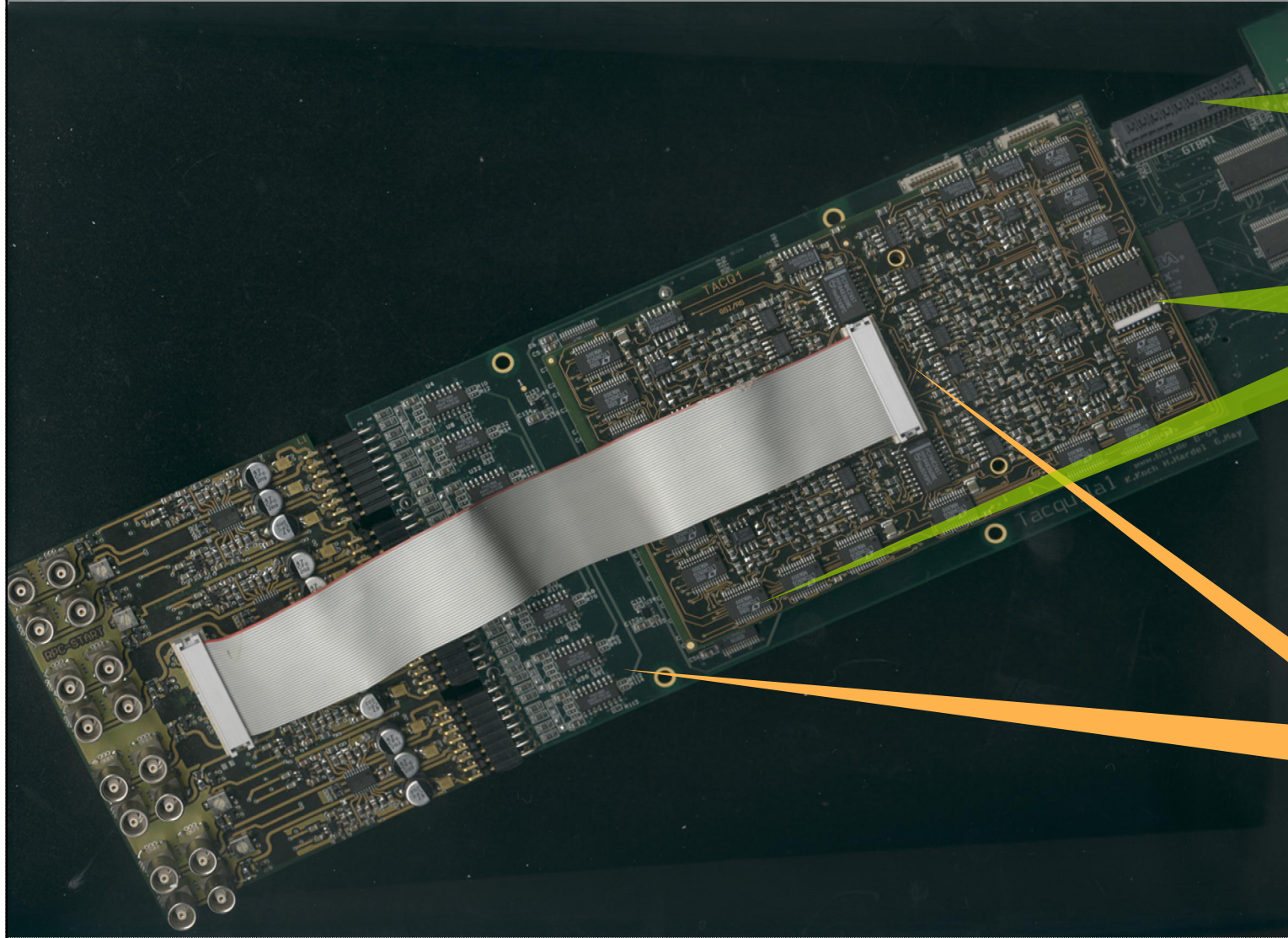
JSI Ljubljana
M. Vencelj

GSI

H. Simon, T. Aumann, Y. Aksyutina,
K. Boretzky, O. Ershova, M. Heil, A. Klimkiewicz,
T. Le Bleis, A. Kelic, R. Plag, R. Reifarth,
D. Rossi, K. Sümmerer, F. Wamers



Example: Precision timing Tacquila System (R³B, FE prototype) - GSI



GTB interface

12 Bit ADCs
10 Bit read ...

TAC Q uila

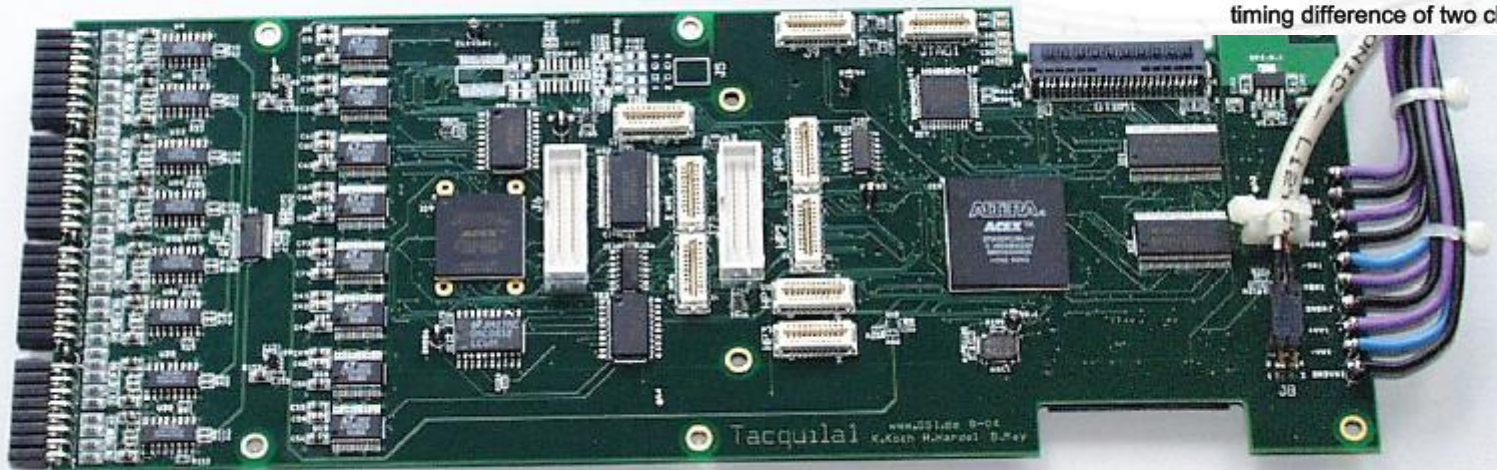
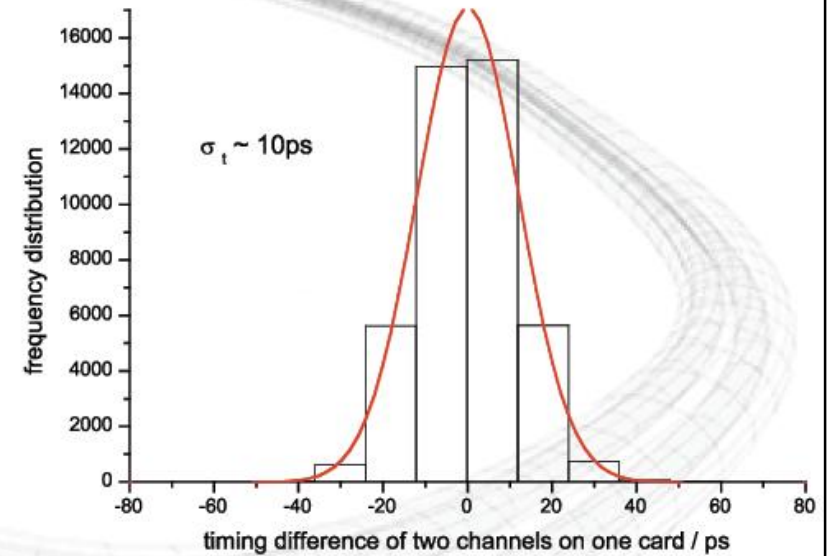


Tacquila

- triggered system

For our application:

- PM signals (LAND, TOF-wall, ...)
- + slow control + monitoring → dedicated front end card





Diamond Detectors

- Super-FRS: $(400 \times 60) \text{ mm}^2$, pitch 1mm, thickness $< 0.2 \text{ mm}$, $\Delta \text{Tof} < 100 \text{ ps}$, rate 10^8 pps
- R³B: $(20 \times 20) \text{ mm}^2$, pitch 0.5mm, thickness $< 0.1 \text{ mm}$, $\Delta \text{Tof} < 50 \text{ ps}$, rate 10^6 pps
- HISPEC/DESPEC- LYCCA: $(60 \times 60) \text{ mm}^2$, thickness $< 0.2 \text{ mm}$, $\Delta \text{Tof} < 50 \text{ ps}$, rate 10^6 pps

BuTiS fibre distribution test bench

BuTiS Master

0.01, 10, e.g. 200 MHz copper

LASER +
AM modulator

opt. fibre (4 * 200GHz bands)

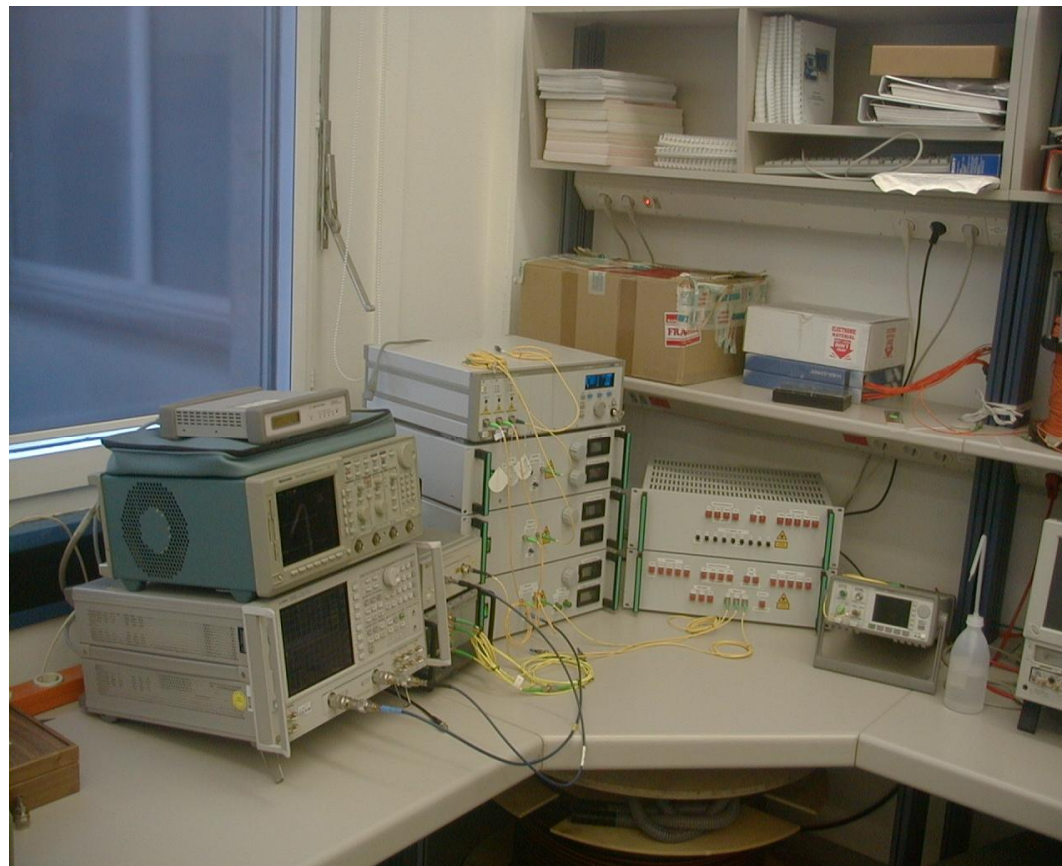
passive splitter
distribution 1..2 km

Demodulator + reflector (4th ch)

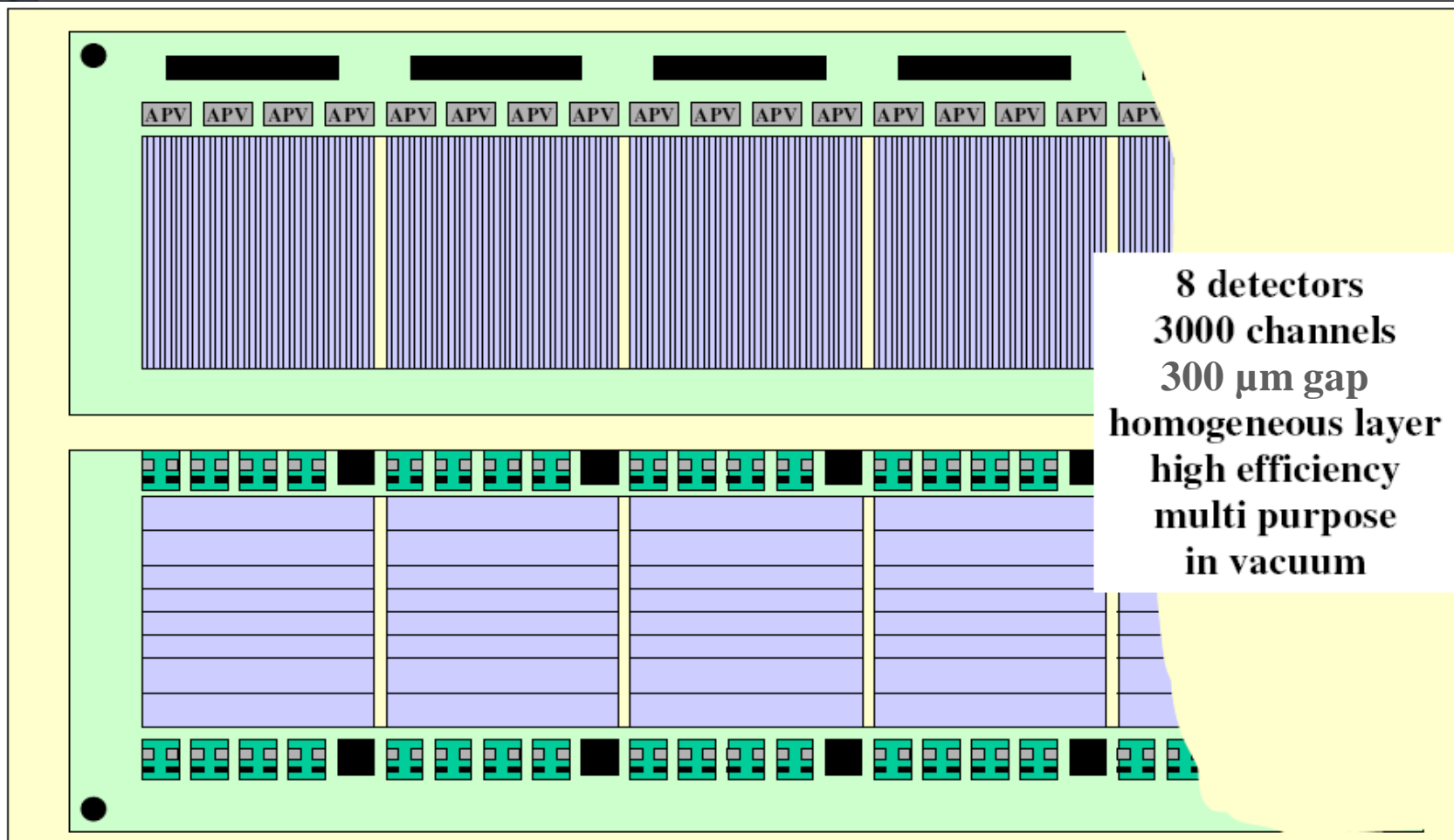
0.01, 10 MHz copper

BuTiS Slave osc.

0.01, 10, e.g. 200 MHz copper → (local TDS) ?

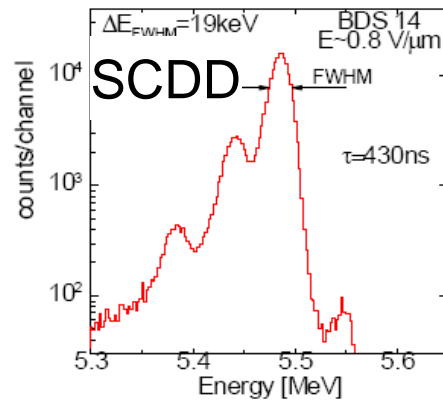
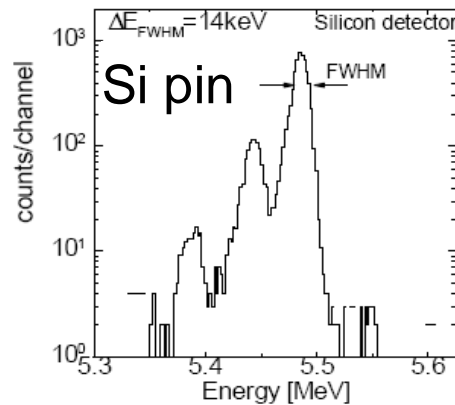
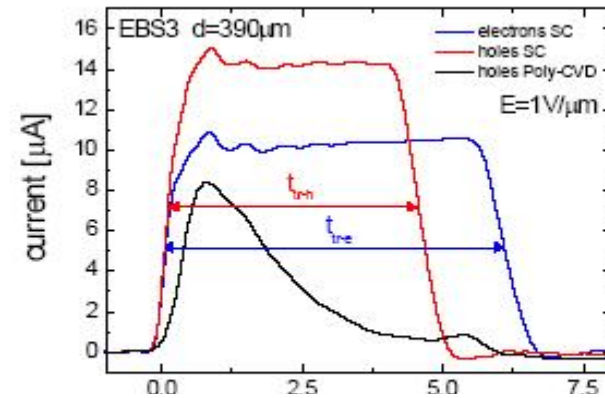
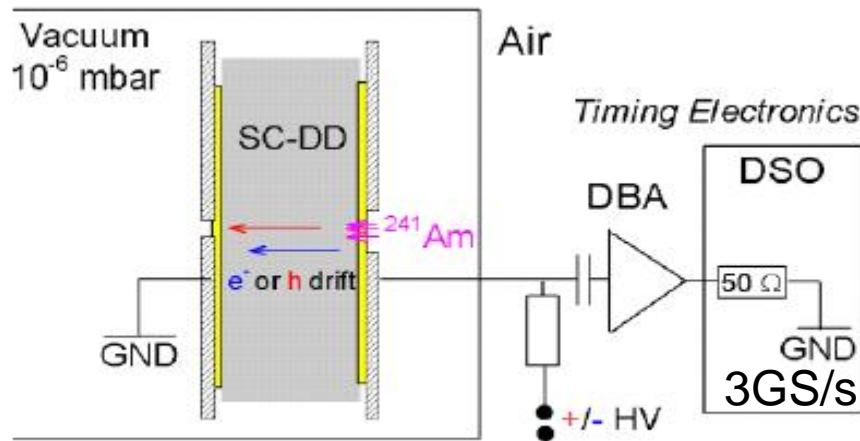


Super-FRS detector layout



Diamond Detectors

$\Delta E \dots$

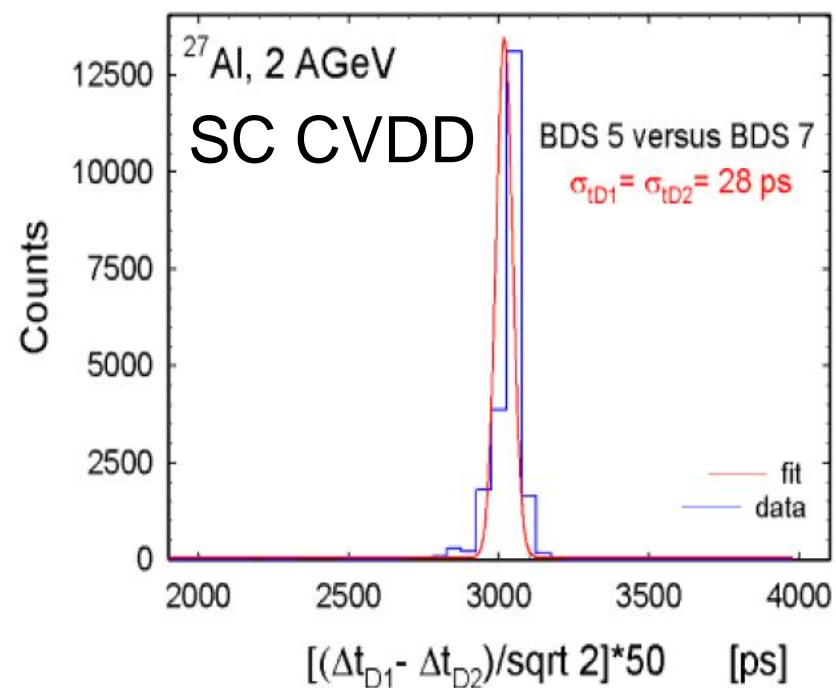
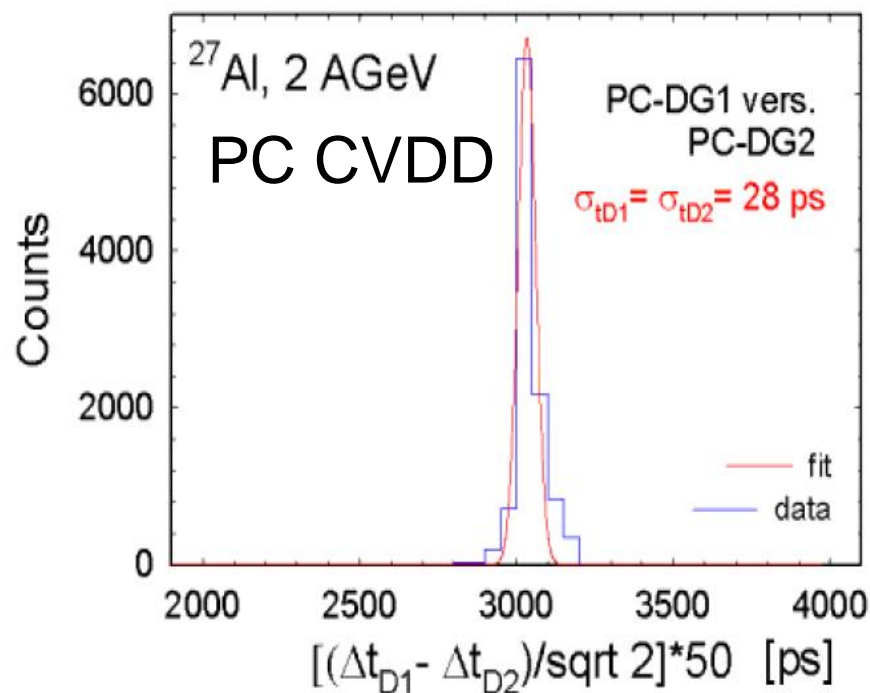


M. Pomorski, E. Berdermann
et al. Nordhia, RD42

- R&D: usage as dE detector, FE electronics !

Diamond Detectors

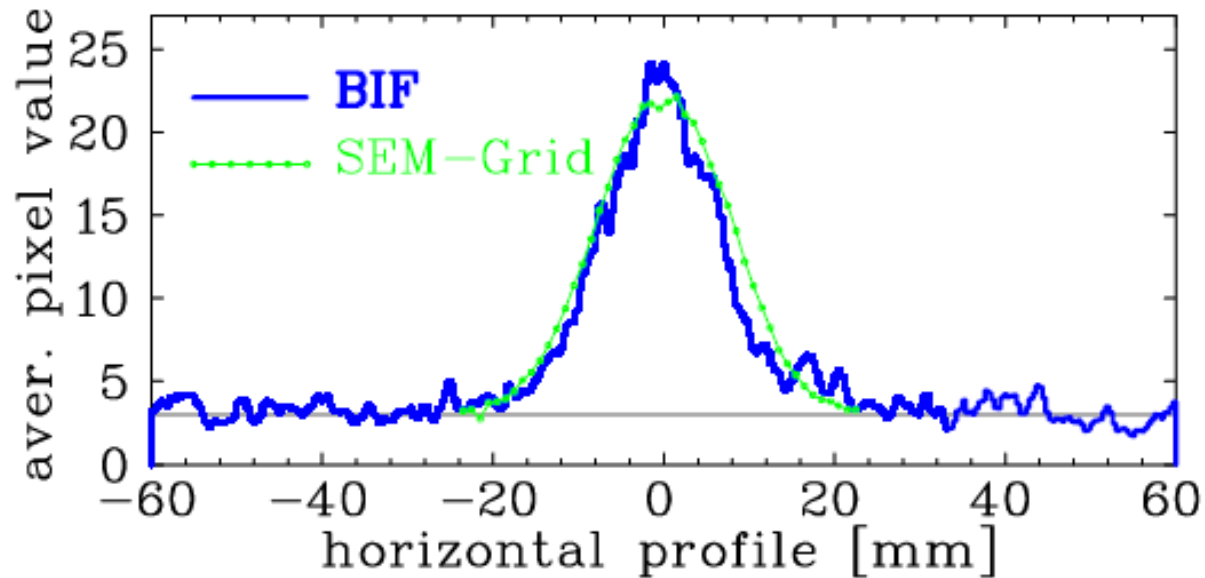
TOF



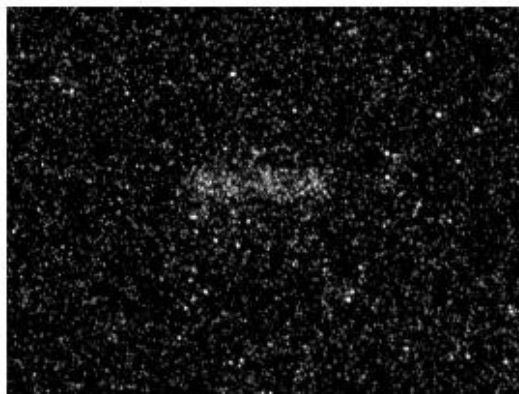
- good timing (eg. R3B req. $\sigma_t \sim 50$ ps)
- R&D: detector geometry strips or pxl / readout electronics
(in about 1m distance)

Applications to beam transport line

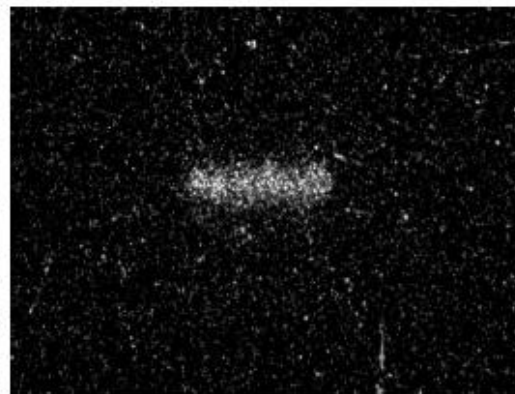
P. Forck / GSI



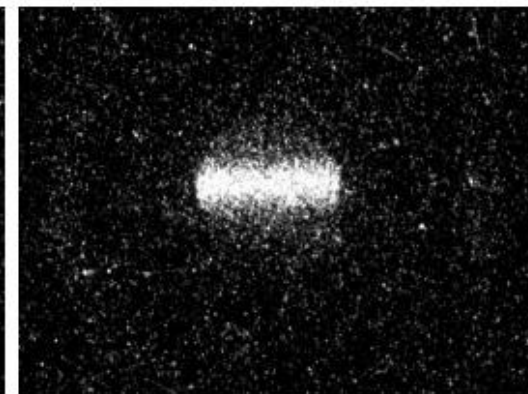
Xe⁴⁸⁺
200MeV/u



(a) Setup ($5 \cdot 10^{-4}$ mbar)

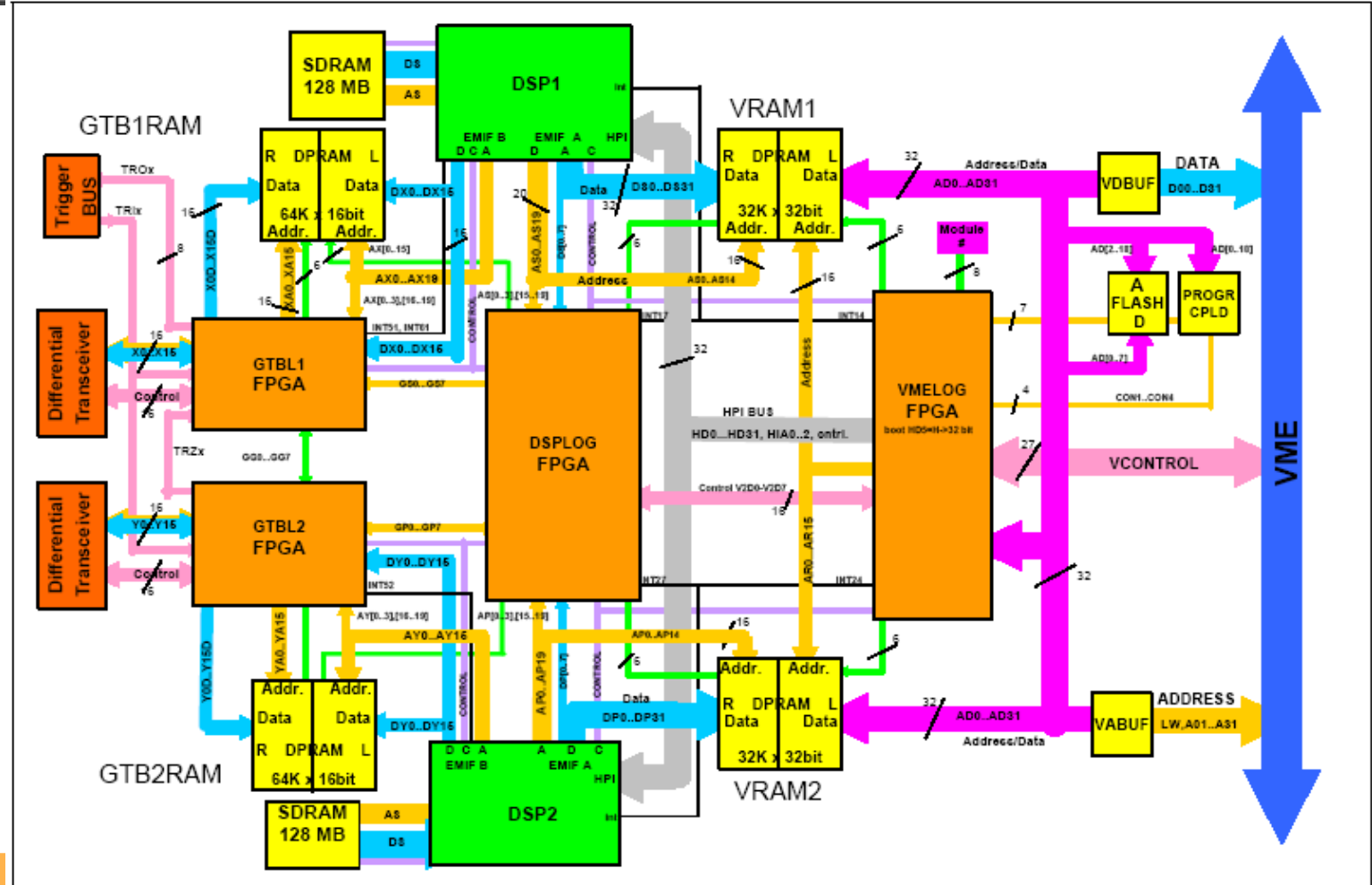


(b) Setup ($1,5 \cdot 10^{-3}$ mbar)



(c) Setup ($7 \cdot 10^{-2}$ mbar)

SAM Readout → VME



Digital data processing
not yet in use !

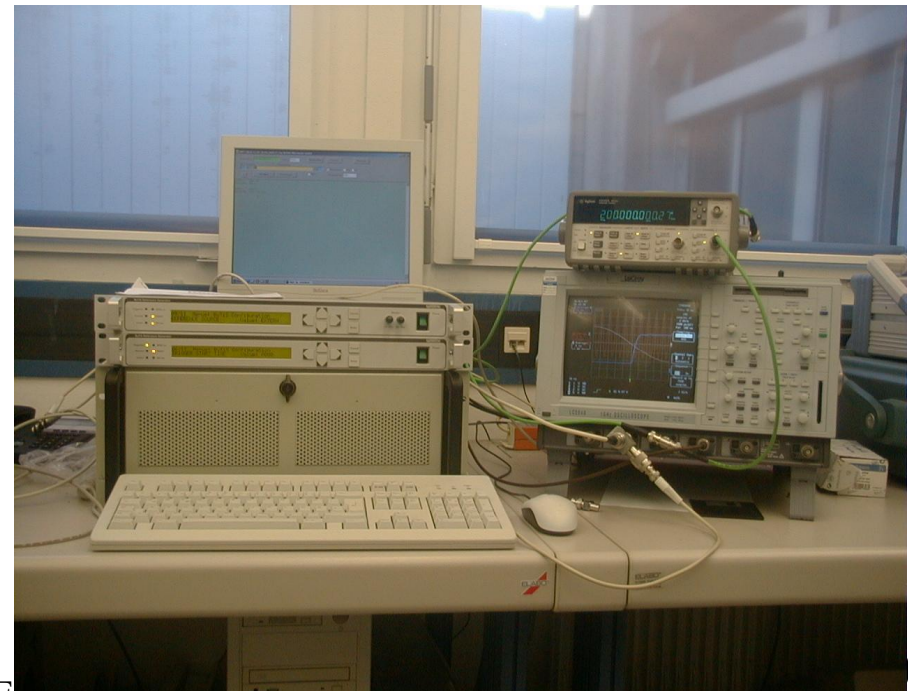
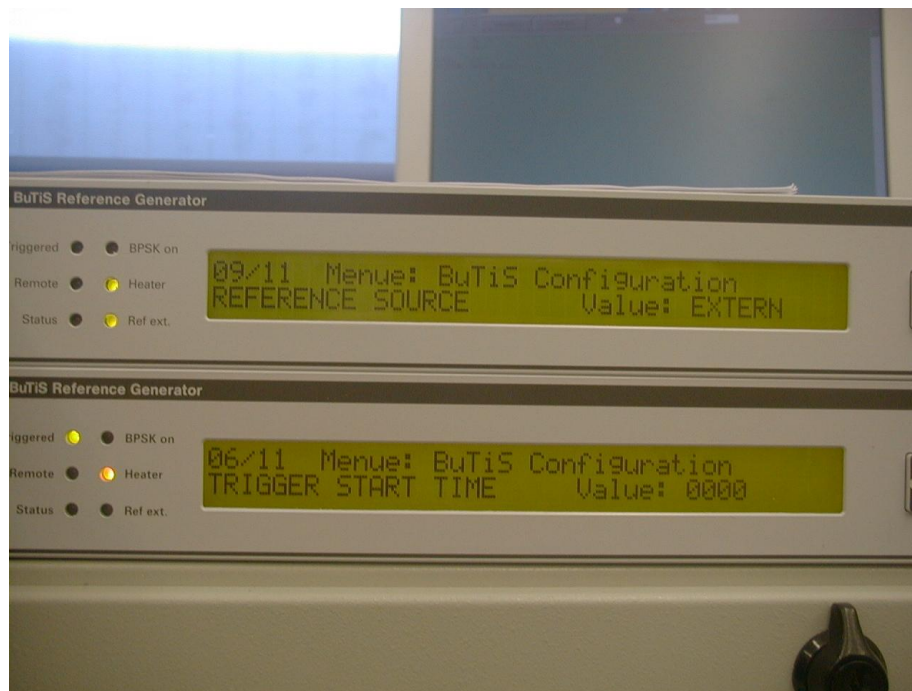
Preparation EoI - Oct 6th 2008



Large Scale → Time distribution system

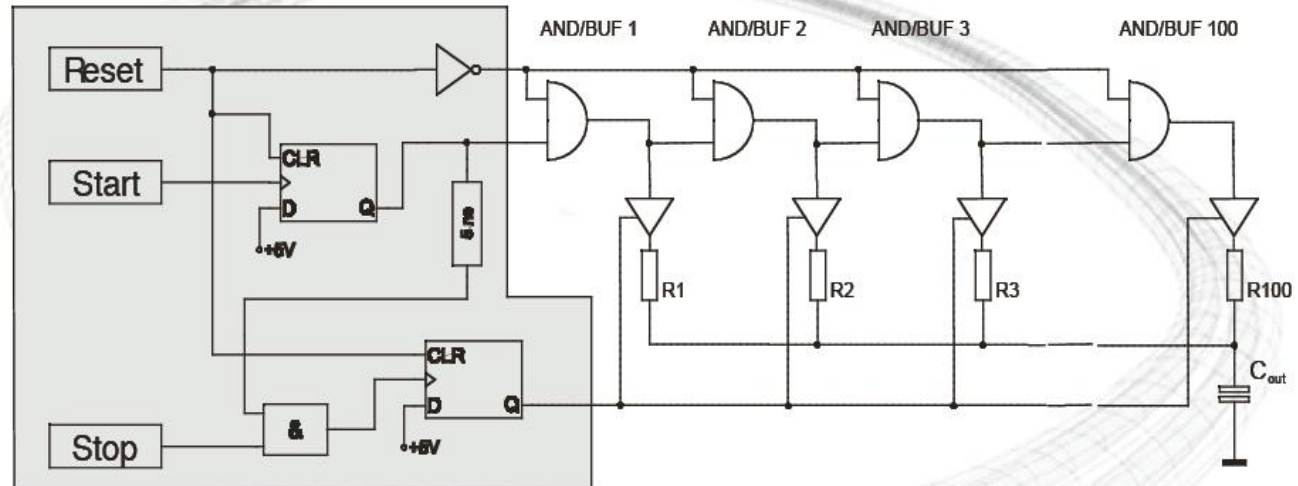
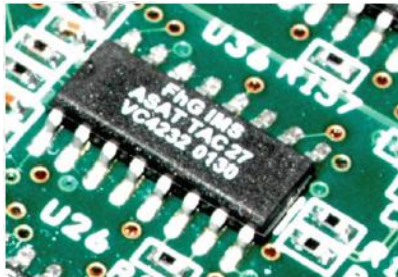
P.Moritz (GSI) in collaboration with Works μ -wave GmbH
J. Agramunt, M. Bellato, P. Coleman-Smith, N. Kurz, H. Schaffner, H.Simon

- Campus wide time distribution system (TDS)
Bunch timing accelerator (BuTiS)
- Exp.: Time of flight between caves / DAQ synchronisation via local TDS
- Synchronous local oscillators (100kHz, 10Mhz, and e.g. 200, 155 or 76 Mhz)
+-100ps/km absolute uncertainty few ps oscillator jitter



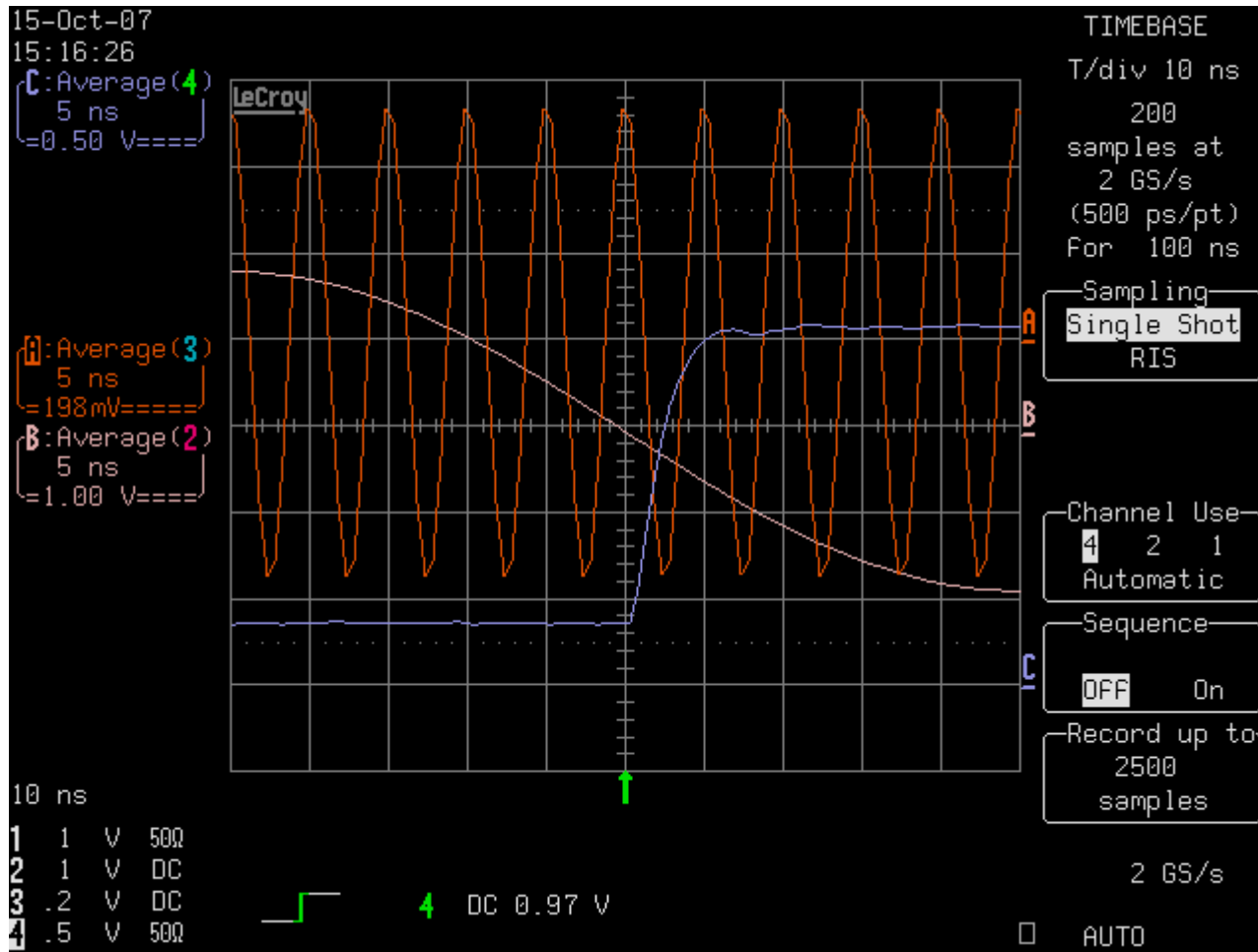
FEE „standard module“ Tacquila

Tac chips
ASIC: FHG Dresden
Concept: GSI



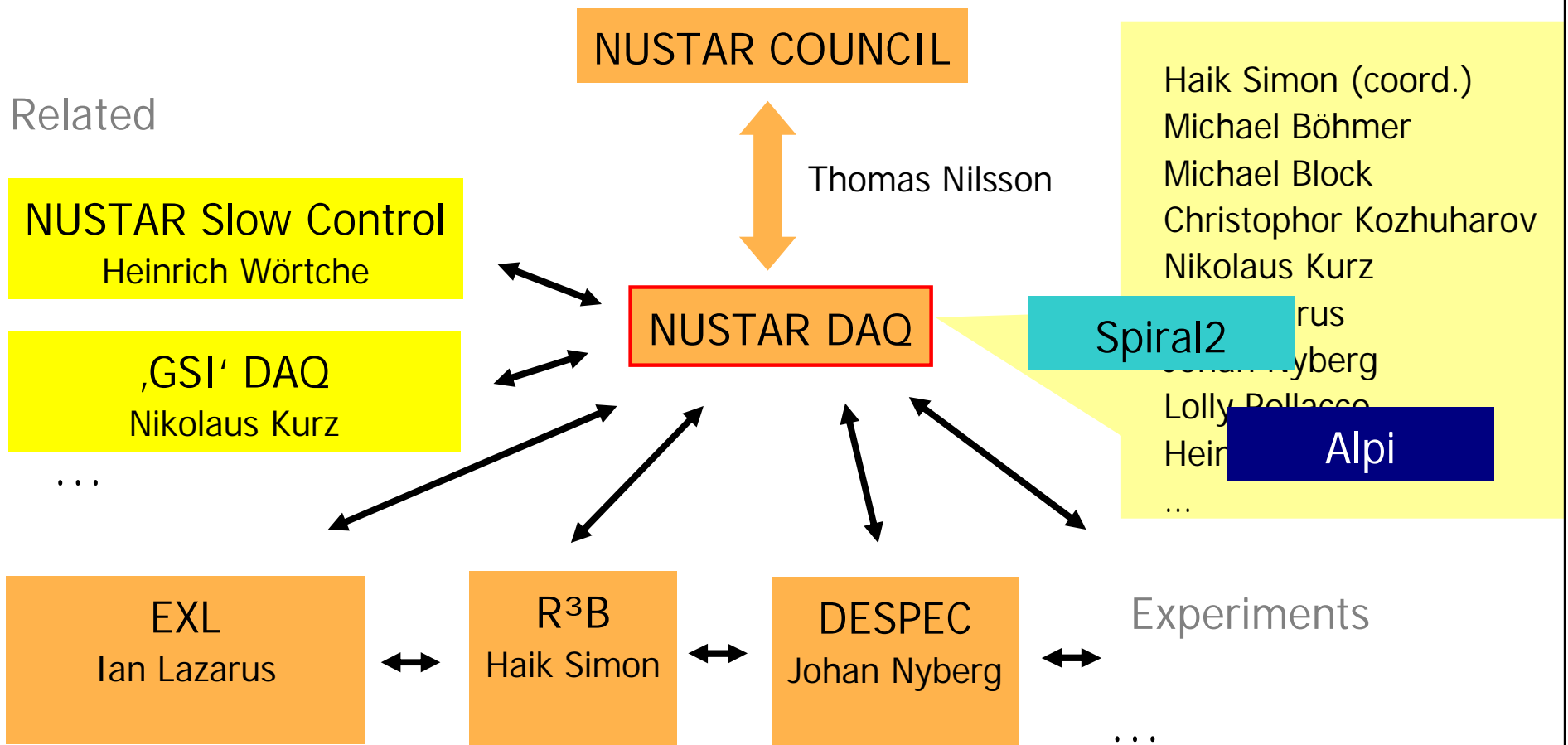
- 1241 chips are available @GSI (9 €/chip)
- 40,000 could be produced from existing wafers and bought (price to be negotiated, one shot)
 - new company for packaging
 - testing about 0.5 €/chip
 - 2800 chips/wafer 2000 €/wafer

BuTiS at work (20071015)

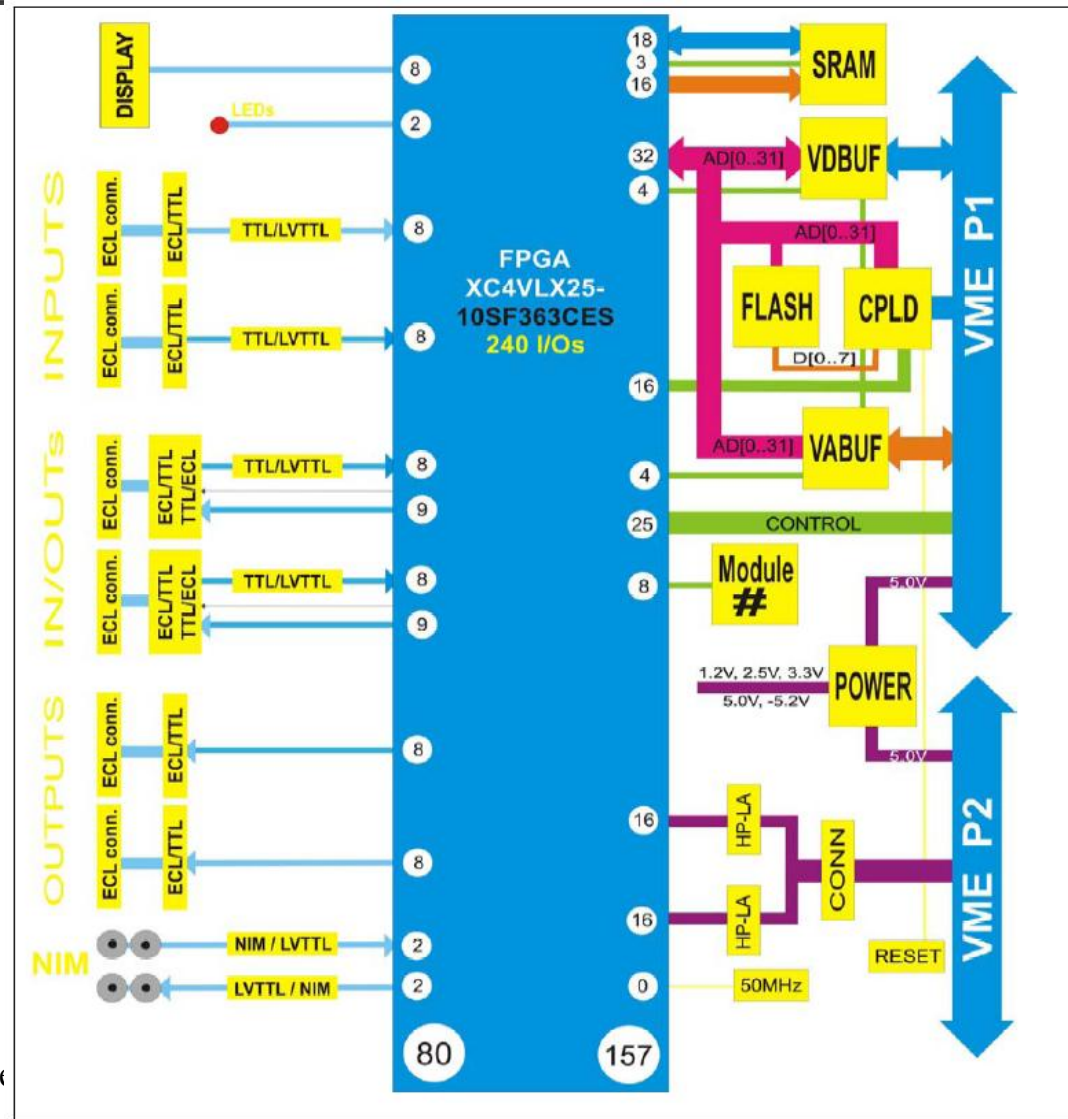
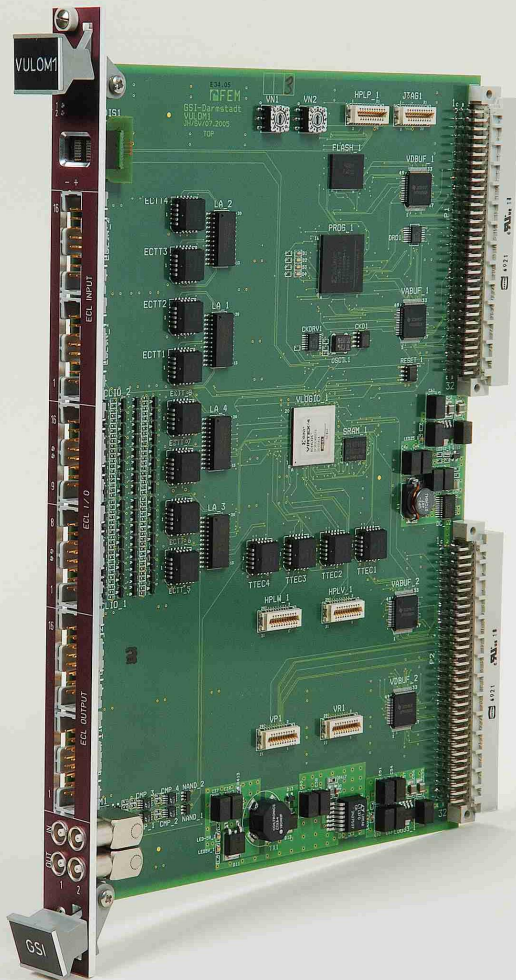


- 10, 200 MHz sine waves (adj. phase)
- T0 pulse for sync.
- very good phase stability
- BuTiS oscillator can run standalone
- about 10k€/system

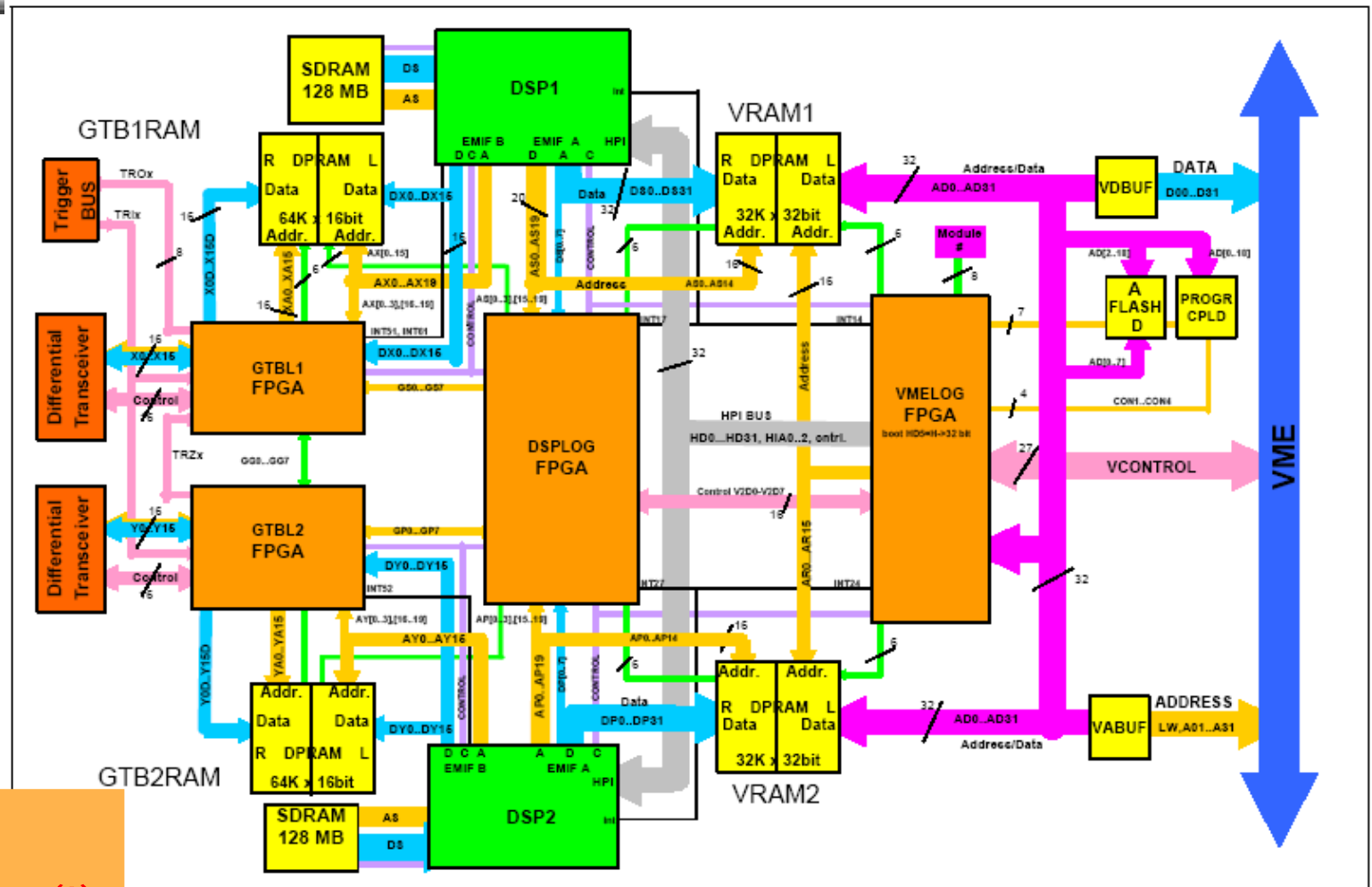
NUSTAR DAQ organisation



Logic Module



SAM Readout → VME → MBS (readout trigger)



FE Trigger !=
MBS trigger (!)

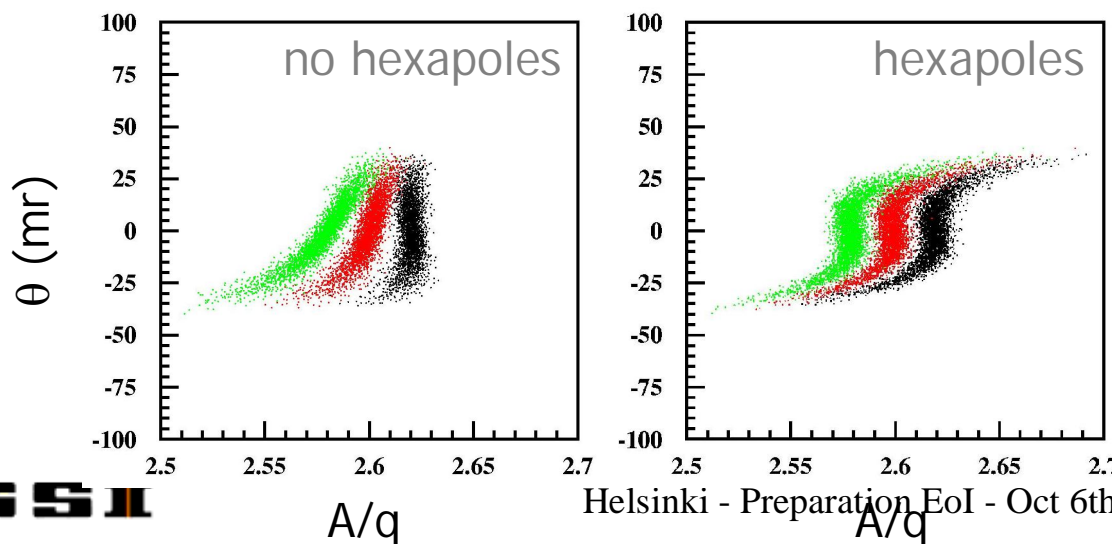
Constraints on detecting system

- Tracking detectors in front of and behind the target (x, y, θ, ϕ)
- Start detector (t_{start})
- Focal plane detector ($x, y, \theta, t_{\text{stop}}, \Delta E, E_{\text{res}}$)

$$\Delta x, \Delta y \sim 1 \text{ mm}$$

$$\Delta \theta, \Delta \phi \sim 2 \text{ mr}$$

$$\Delta \text{Tof} \sim 150 \text{ ps}$$



$^{132}\text{-nSn}$ knocked out fragments

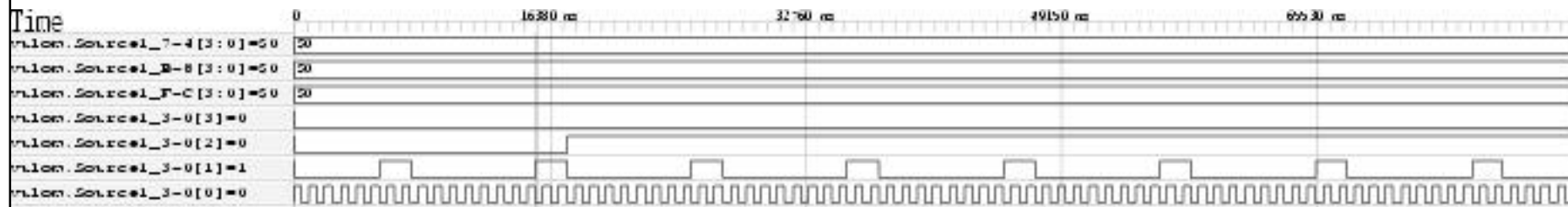
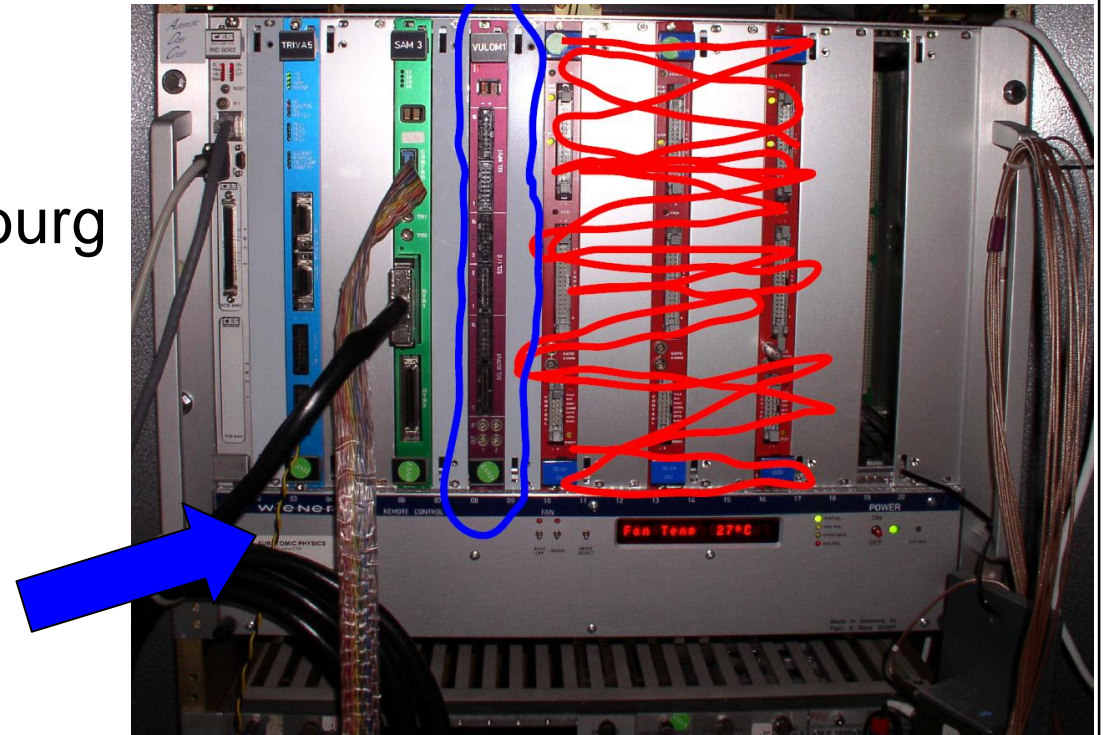
Monitoring integrated electronics (example: Vulom Trigger Electr.)

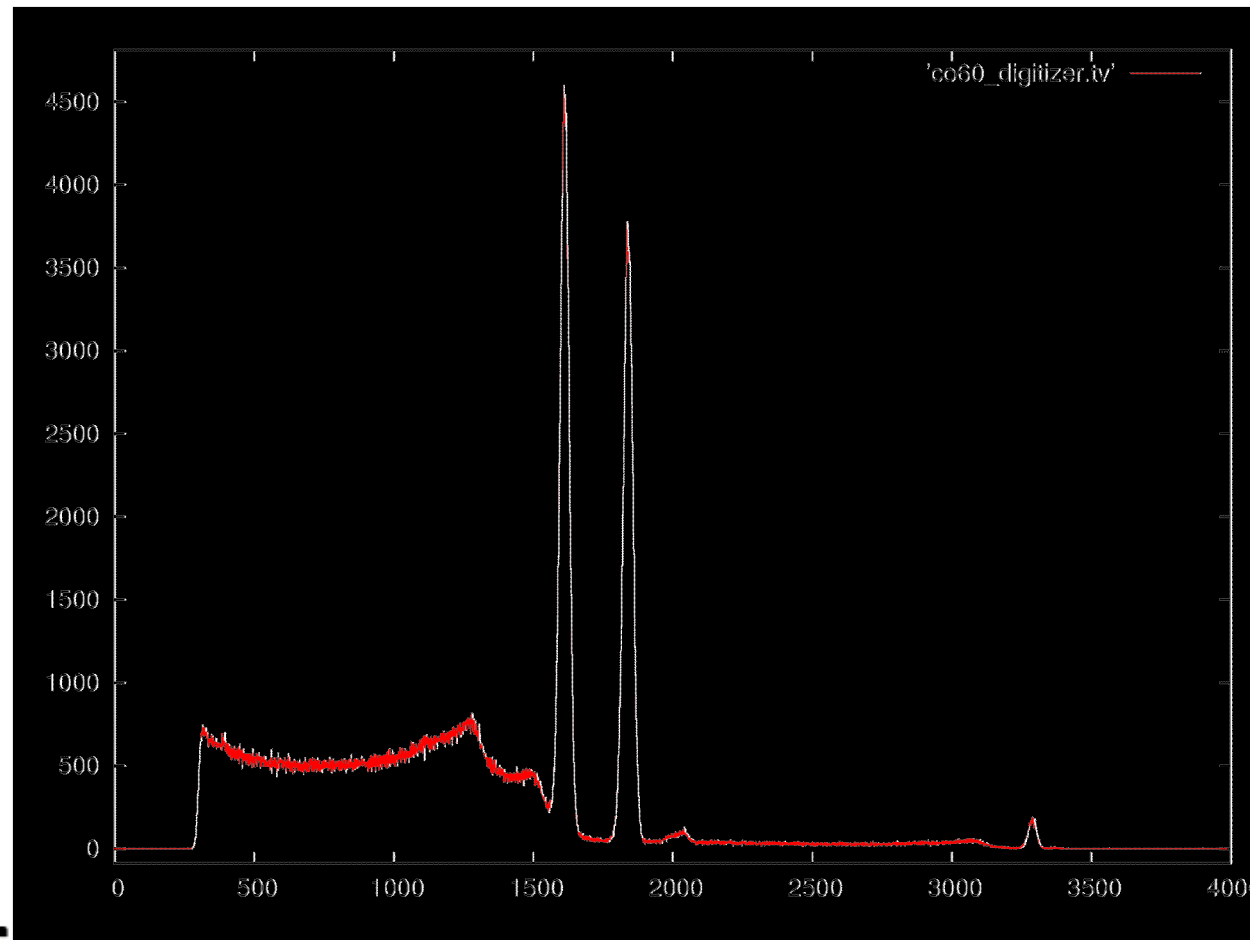
M.Fuhrmann, H.Simon

Logic Module (FPGA)
Master Thesis: Softscope
Univ of Applied Science Coburg

→GTKwave output
→programming
via DAQ channel

Test: Summer student
P. Lubberdink (KVI)





- oversampling
2GS, intelligent
averaging
- comparable
resolution to
conventional
spectroscopy
setup ...

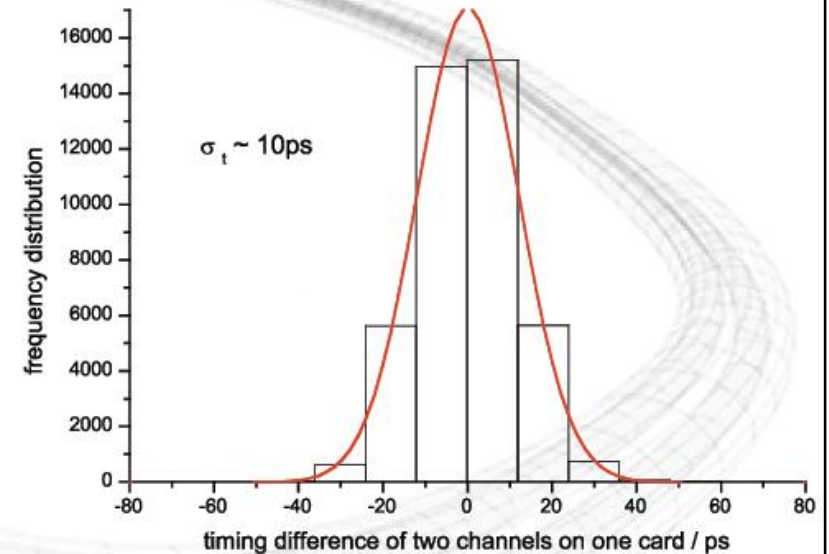
ASIC: System Design

fast timing

G. May, H.Simon (GSI)

Application (100 - 1000 ch.)

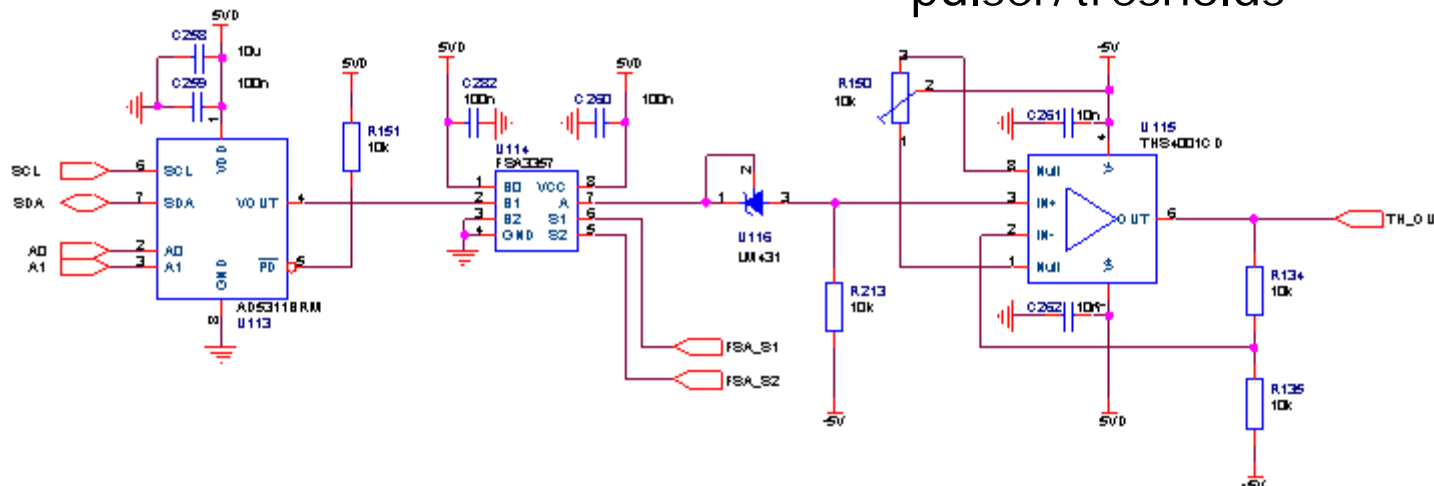
- **PMT signals (TOF, LAND ...)**
 - **+ slow control + monitoring → dedicated front end card**
 - **Triggering facilities (OR/Mult./Anal. Sum)**
- **TAC ASIC to be replaced with New CBM DLL chip**



H. Deppe (GSI)

pulser/tresholds

E.g.
I2C controlled
discriminators



Example: Current LAND electronics ~ 600 ch

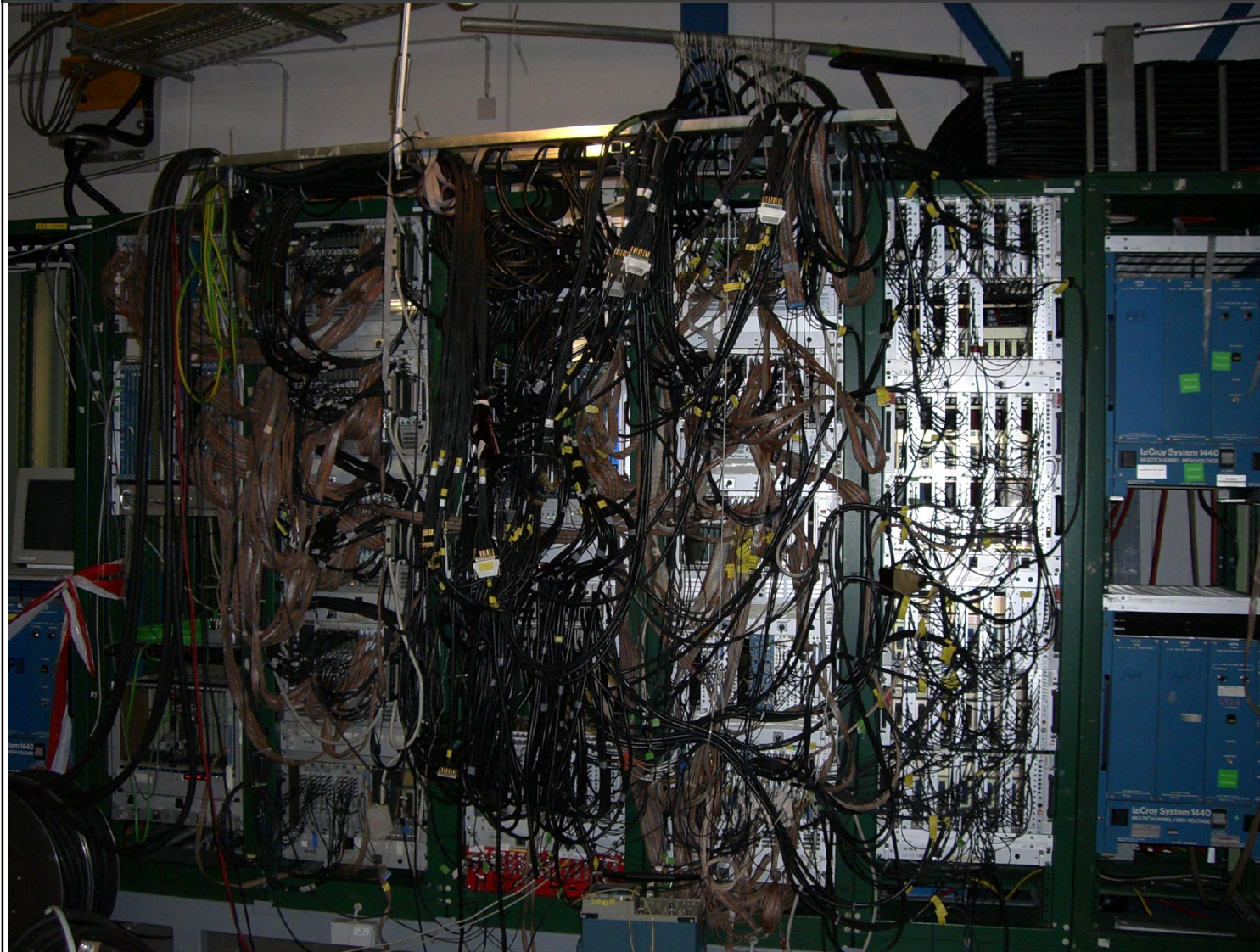
G. May, K. Koch, H.Simon (GSI)

New design

= 30 Tacquila cards with
LAND FEE +
2 VME helper
modules +
1 VME CPU
+ 10 VME QDCs
+ 3 HV bins

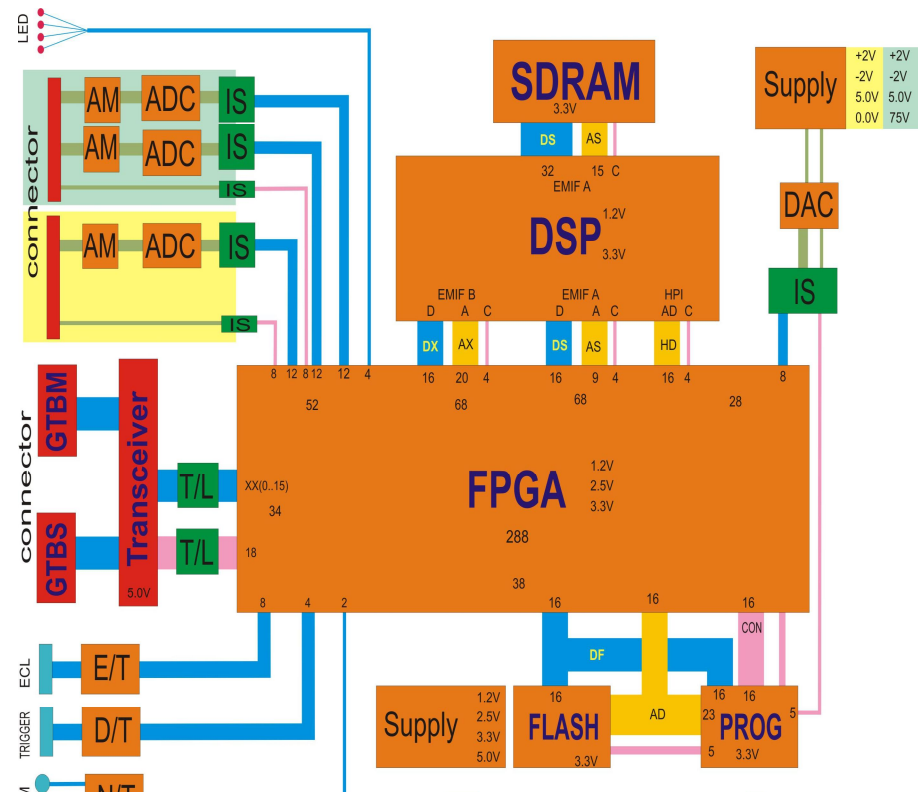
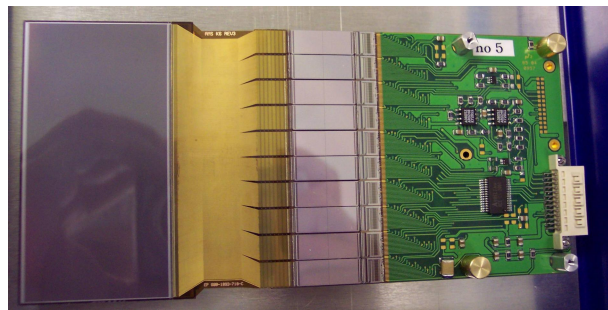
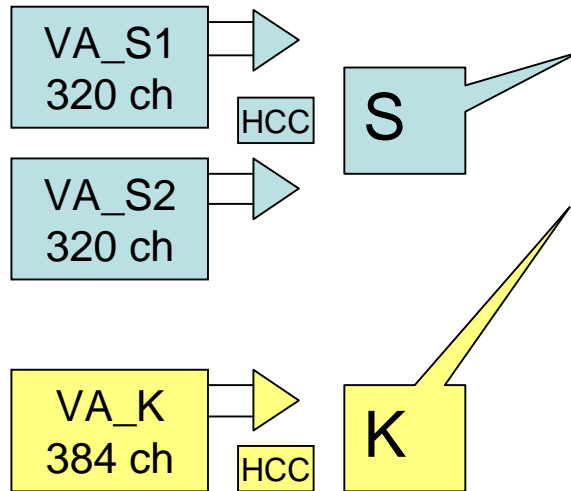
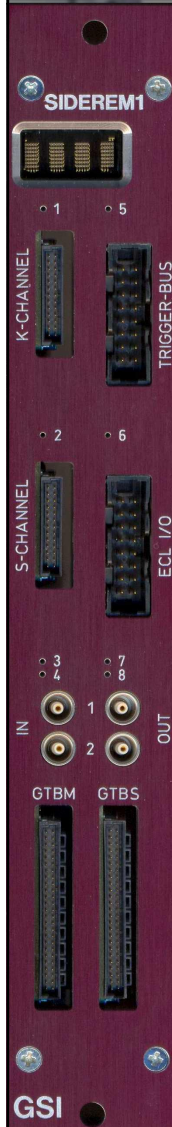
Boils down to
1 + ϵ crates !

FAIR



Silicon Strip **DE**tector **RE**adout **M**odule (SIDEREM)

J. Hofmann, W. Ott, N.Kurz (GSI)



12 Bit ADC
 DSP
 FPGA

ca. 80 μ s readout time (5MHz)
 TMS320VC6414 (TI)
 Virtex-4 LX25 (Xilinx)

NT = NIM / LVTTL **DT** = TTL / LVTTL **IS** = isolator